Cover image:
MIPS R3000 Custom Chip
http://commons.wikimedia.org/wiki/File:RCP-NUS_01.jpg

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1.0 Introduction

There are number of excellent, comprehensive, and in-depth texts on MIPS assembly language programming. This is not one of them.

The purpose of this text is to provide a simple and free reference for university level programming and architecture units that include a brief section covering MIPS assembly language programming. The text assumes usage of the QtSpim simulator. An appendix is included that covers the download, installation, and basic use of the QtSpim simulator.

The scope of this text addresses basic MIPS assembly language programming including instruction set usage, stacks, procedure/function calls, QtSpim simulator system services, multiple dimension arrays, and basic recursion.

1.1 Additional References

Some key references for additional information are listed below:

- *MIPS Software Users Manual*, MIPS Technologies, Inc.
- *Computer Organization and Design: The Hardware/Software Interface*, Hennessy and Patterson

More information regarding these references can be found on the Internet.
Chapter 1.0  ► Introduction
2.0  **MIPS Architecture Overview**

This chapter presents a basic, general overview of the architecture of the MIPS processor.

The MIPS architecture is a Reduced Instruction Set Computer (RISC). This means that there is a smaller number of instructions that use a uniform instruction encoding format. Each instruction/operation does one thing (memory access, computation, conditional, etc.). The idea is to make the lesser number of instructions execute faster. In general RISC architectures, and specifically the MIPS architecture, are designed for high-speed implementations.

2.1  **Architecture Overview**

The basic components of a computer include a Central Processing Unit (CPU), Random Access Memory (RAM), Disk Drive, and Input/Output devices (i.e., screen and keyboard), and an interconnection referred to as BUS.

A very basic diagram of a computer architecture is as follows:

![Illustration 1: Computer Architecture](image)
Programs and data are typically stored on the disk drive. When a program is executed, it must be copied from the disk drive into the RAM memory. The CPU executes the program from RAM. This is similar to storing a term paper on the disk drive, and when writing/editing the term paper, it is copied from the disk drive into memory. When done, the updated version is stored back to the disk drive.

### 2.2 Data Types/Sizes

The basic data types include integer, floating point, and characters.

I architecture supports data storage sizes of byte, halfword (sometimes referred to as just *half*), or word sizes. Floating point must be in either word (32-bit) or double word (64-bit) size. Character data is typically a byte and a string is a series of sequential bytes.

The MIPS architecture supports the following data/memory sizes:

<table>
<thead>
<tr>
<th>Name</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>byte</td>
<td>8-bit integer</td>
</tr>
<tr>
<td>halfword</td>
<td>16-bit integer</td>
</tr>
<tr>
<td>word</td>
<td>32-bit integer</td>
</tr>
<tr>
<td>float</td>
<td>32-bit floating-point number</td>
</tr>
<tr>
<td>double</td>
<td>64-bit floating-point number</td>
</tr>
</tbody>
</table>

The halfword is often referred to as just *half*. Lists or arrays (sets of memory) can be reserved in any of these types. In addition, an arbitrary number of bytes can be defined with the "space" directive.

### 2.3 Memory

Memory can be viewed as a series of bytes, one after another. That is, memory is *byte addressable*. This means each memory address holds one byte of information. To store a word, four bytes are required which use four memory addresses.

Additionally, the MIPS architecture as simulated in QtSpim is *little-endian*. This means that the Least Significant Byte (LSB) is stored in the lowest memory address. The Most Significant Byte (MSB) is stored in the highest memory location.
For a word (32-bits), the MSB and LSB are allocated as shown below.

<table>
<thead>
<tr>
<th>31</th>
<th>30</th>
<th>29</th>
<th>28</th>
<th>27</th>
<th>26</th>
<th>25</th>
<th>24</th>
<th>23</th>
<th>22</th>
<th>21</th>
<th>20</th>
<th>19</th>
<th>18</th>
<th>17</th>
<th>16</th>
<th>15</th>
<th>14</th>
<th>13</th>
<th>12</th>
<th>11</th>
<th>10</th>
<th>9</th>
<th>8</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>MSB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>LSB</td>
</tr>
</tbody>
</table>

For example, assuming the following declarations:

```
num1: .word 42
num2: .word 5000000
```

Recall that $42_{10}$ in hex, word size, is $0x0000002A$ and $5,000,000_{10}$ in hex, word size, is $0x004C4B40$.

For a little-endian architecture, the memory picture would be as follows:

<table>
<thead>
<tr>
<th>variable name</th>
<th>value</th>
<th>address</th>
</tr>
</thead>
<tbody>
<tr>
<td>Num2</td>
<td></td>
<td>0x100100C</td>
</tr>
<tr>
<td></td>
<td>00</td>
<td>0x100100B</td>
</tr>
<tr>
<td></td>
<td>4C</td>
<td>0x100100A</td>
</tr>
<tr>
<td>Num1</td>
<td></td>
<td>0x1001009</td>
</tr>
<tr>
<td></td>
<td>4B</td>
<td>0x1001008</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>0x1001007</td>
</tr>
<tr>
<td></td>
<td>00</td>
<td>0x1001006</td>
</tr>
<tr>
<td></td>
<td>00</td>
<td>0x1001005</td>
</tr>
<tr>
<td></td>
<td>2A</td>
<td>0x1001004</td>
</tr>
<tr>
<td></td>
<td>?</td>
<td>0x1001003</td>
</tr>
</tbody>
</table>

Based on the little-endian architecture, the LSB is stored in the lowest memory address and the MSB is stored in the highest memory location.
2.4 Memory Layout

The general memory layout for a program is as shown:

```
+----------------+             +----------------+
| high memory    | stack            | low memory      |
|                | heap             | reserved        |
|                | initialized data |                |
|                | data             |                |
|                | text (code)      |                |
```

The reserved section is not available to user programs. The text (or code) section is where the machine language (i.e., the 1's and 0's that represent the code) is stored. The data section is where the initialized data is stored. This include declared variables that have been provided an initial value at assemble time. The uninitialized data section is where declared variables that have not been provided an initial value are stored. If accessed before being set, the value will not be meaningful. The heap is where dynamically allocated data will be stored (if requested). The stack starts in high memory and grows downward.

The QtSpim simulator does not distinguish between the initialized and uninitialized data sections. Later sections will provide additional detail for the text and data sections.

2.5 CPU Registers

A CPU register, or just register, is a temporary storage or working location built into the CPU itself (separate from memory). Computations are typically performed by the CPU using registers.

The MIPS has 32, 32-bit integer registers ($0$ through $31$) and 32, 32-bit floating point registers ($f0$ through $f31$). Some of the integer registers are used for special purposes. For example, $s29$ is dedicated for use as the stack pointer register, referred to as $sp$. 
Chapter 2.0  ►  MIPS Architecture Overview

The registers available and typical register usage is described in the following table.

<table>
<thead>
<tr>
<th>Register Name</th>
<th>Register Number</th>
<th>Register Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>$zero</td>
<td>$0</td>
<td>Hardware set to 0</td>
</tr>
<tr>
<td>$at</td>
<td>$1</td>
<td>Assembler temporary</td>
</tr>
<tr>
<td>$v0 - $v1</td>
<td>$2 - $3</td>
<td>Function result (low/high)</td>
</tr>
<tr>
<td>$a0 - $a3</td>
<td>$4 - $7</td>
<td>Argument Register 1</td>
</tr>
<tr>
<td>$t0 - $t7</td>
<td>$8 - $15</td>
<td>Temporary registers</td>
</tr>
<tr>
<td>$s0 - $s7</td>
<td>$16 - $23</td>
<td>Saved registers</td>
</tr>
<tr>
<td>$t8 - $t9</td>
<td>$24 - $25</td>
<td>Temporary registers</td>
</tr>
<tr>
<td>$k0 - $k1</td>
<td>$26 - $27</td>
<td>Reserved for OS kernel</td>
</tr>
<tr>
<td>$gp</td>
<td>$28</td>
<td>Global pointer</td>
</tr>
<tr>
<td>$sp</td>
<td>$29</td>
<td>Stack pointer</td>
</tr>
<tr>
<td>$fp</td>
<td>$30</td>
<td>Frame pointer</td>
</tr>
<tr>
<td>$ra</td>
<td>$31</td>
<td>Return address</td>
</tr>
</tbody>
</table>

The register names convey specific usage information. The register names will used in the remainder of this document. Further sections will expand on register usage conventions and address the 'temporary' and 'saved' registers.

### 2.5.1 Reserved Registers

The following reserved registers should not be used in user programs.

<table>
<thead>
<tr>
<th>Register Name</th>
<th>Register Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>$k0 - $k1</td>
<td>Reserved for use by the Operating System</td>
</tr>
<tr>
<td>$at</td>
<td>Assembler temporary</td>
</tr>
<tr>
<td>$gp</td>
<td>Global pointer</td>
</tr>
<tr>
<td>$epc</td>
<td>Exception program counter</td>
</tr>
</tbody>
</table>
Chapter 2.0  MIPС Architecture Overview

The $k0 and $k1 registers are reserved for use by the operating system and should not be used in user programs. The $at register is used by the assembler and should not be used in user programs. The $gp register is used to point to global data (as needed) and should not be used in user programs.

2.5.2 Miscellaneous Registers

In addition to the previously listed registers, there are some miscellaneous registers which are listed in the table:

<table>
<thead>
<tr>
<th>Register Name</th>
<th>Register Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>$pc</td>
<td>Program counter</td>
</tr>
<tr>
<td>$status</td>
<td>Status Register</td>
</tr>
<tr>
<td>$cause</td>
<td>Exception cause register</td>
</tr>
<tr>
<td>$hi</td>
<td>Used for some multiple/divide operations</td>
</tr>
<tr>
<td>$lo</td>
<td>Used for some multiple/divide operations</td>
</tr>
</tbody>
</table>

The $pc or program counter register points to the next instruction to be executed and is automatically updated by the CPU after instruction are executed. This register is not typically accessed directly by user programs.

The $status or status register is the processor status register and is updated after each instruction by the CPU. This register is not typically directly accessed by user programs.

The $cause or exception cause register is used by the CPU in the event of an exception or unexpected interruption in program control flow. Examples of exceptions include division by 0, attempting to access an illegal memory address, or attempting to execute an invalid instruction (e.g., trying to execute a data item instead of code).

The $hi and $lo registers are used by some specialized multiply and divide instructions. For example, a multiply of two 32-bit values can generate a 64-bit results, which is stored in $hi and $lo (32-bits each or a total of 64-bits).


2.6 CPU / FPU Core Configuration

The following diagram shows a basic configuration of the MIPS processor internal architecture.

![MIPS Chip Core Configuration]

The FPU (floating point unit) is also referred to as the FPU co-processor or simply co-processor 1.
3.0 Data Representation

Data representation refers to how information is stored within the computer. There is a specific method for storing integers which is different than storing floating point values which is different than storing characters. This chapter presents a brief summary of the integer, floating-point, and ASCII representation schemes. It is assumed the reader is already generally familiar with the binary, decimal, and hexadecimal numbering systems.

3.1 Integer Representation

Representing integer numbers refers to how the computer stores or represents a number in memory. As you know, the computer represents numbers in binary. However, the computer has a limited amount of space that can be used for each number or variable. This directly impacts the size, or range, of the number that can be represented. For example, a byte (8 bits) can be used to represent \(2^8\) or 256 different numbers. Those 256 different numbers can be unsigned (all positive) in which case we can represent any number between 0 and 255 (inclusive). If we choose signed (positive and negative), then we can represent any number between -128 and +127 (inclusive).

If that range is not large enough to handle the intended values, a larger size must be used. For example, a halfword (16 bits) can be used to represent \(2^{16}\) or 65,536 different numbers, and a word can be used to represent \(2^{32}\) or 4,294,967,296 different numbers. So, if you wanted to store a value of 100,000 then a word would be required.

The following table shows the ranges associated with typical sizes:

<table>
<thead>
<tr>
<th>Size</th>
<th>Size</th>
<th>Unsigned Range</th>
<th>Signed Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bytes (8 bits)</td>
<td>(2^8)</td>
<td>0 to 255</td>
<td>-128 to +127</td>
</tr>
<tr>
<td>Halfwords (16 bits)</td>
<td>(2^{16})</td>
<td>0 to 65,535</td>
<td>-32,768 to +32,767</td>
</tr>
<tr>
<td>Words (32 bits)</td>
<td>(2^{32})</td>
<td>0 to 4,294,967,295</td>
<td>-2,147,483,648 to +2,147,483,647</td>
</tr>
</tbody>
</table>
Chapter 3.0  ▷ Data Representation

In order to determine if a value can be represented, you will need to know the size of
storage element (byte, halfword, word) being used and if the values are signed or
unsigned values.

- For representing unsigned values within the range of a given storage size,
  standard binary is used.
- For representing signed values within the range, two's compliment is used.
  Specifically, the two's compliment encoding process applies to the values in the
  negative range. For values within the positive range, standard binary is used.

Additional detail regarding two's compliment is provided in the next section.

For example, the unsigned byte range can be represented using a number line as follows:

```
0  127
```

For example, the signed byte range can also be represented using a number line as
follows:

```
-128  0  +127
```

The same concept applies to halfwords and words with larger ranges.

Since unsigned values have a different, positive only, range than signed values, there is
some overlap between the values. For example when the unsigned and signed values
are within the overlapping positive range (0 to +127):

- A signed byte representation of 12 is 0x0C_{16}
- An unsigned byte representation of 12 is also 0x0C_{16}

When the unsigned and signed values are outside the overlapping range:

- A signed byte representation of -15 is 0xF1_{16}
- An unsigned byte representation of 241 is also 0xF1_{16}

This overlap can cause confusion unless the data types are clearly and correctly defined.
3.1.1 Two's Compliment

The following describes how to find the two's compliment representation for negative values.

To take the two's compliment of a number:

1. take the one's compliment (negate)
2. add 1 (in binary)

The same process is used to encode a decimal value into two's compliment and from two's compliment back to decimal. The following sections provide some examples.

3.1.2 Byte Example

For example, to find the byte size, two's compliment representation of -9 and -12.

<table>
<thead>
<tr>
<th>Step 1</th>
<th>Step 2</th>
<th>-9 (in hex)</th>
<th>-12 (in hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>11110011</td>
<td>11110111</td>
<td>F7</td>
<td>F4</td>
</tr>
</tbody>
</table>

Note, all bits for the given size, byte in this example, must be specified.

3.1.3 Halfword Example

To find the halfword size, two's compliment representation of -18 and -40.

<table>
<thead>
<tr>
<th>Step 1</th>
<th>Step 2</th>
<th>-18 (hex)</th>
<th>-40 (hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>111101110110</td>
<td>1111111111101111</td>
<td>FFEE</td>
<td>FFD8</td>
</tr>
</tbody>
</table>

Note, all bits for the given size, halfwords in these examples, must be specified.
3.2 Unsigned and Signed Addition

As previously noted, the unsigned and signed representations may provide different interpretations for the final value being represented. However, the addition and subtraction operations are the same. For example:

\[
\begin{array}{c|c}
241 & 11110001 \\
+ & 7 \\
\hline
248 & 11110000 \\
\end{array}
\]

\[
\begin{array}{c|c}
-15 & 11110001 \\
+ & 7 \\
\hline
-8 & 11110000 \\
\end{array}
\]

\[
248 = F8 \\
-8 = F8 \\
\]

The final result of 0xF8 may be interpreted as 248 for unsigned representation and -8 for a signed representation.

Additionally, 0xF8₁₆ is the \(^{\circ}\) (degree symbol) in the ASCII table.

As such, it is very important to have a clear definition of the sizes (byte, halfword, word, etc.) and types (signed, unsigned) of data for the operations being performed.

3.3 Floating-point Representation

The representation issues for floating points numbers are more complex. There are a series of floating point representations for various ranges of the value. For simplicity, we will only look primarily at the IEEE 754 32-bit floating-point standard.

3.3.1 IEEE 32-bit Representation

The IEEE 754 32-bit floating-point standard is defined as follows:

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| s  | biased exponent | fraction |

Where \( s \) is the sign (0 => positive and 1 => negative). When representing floating point values, the first step is to convert floating point value into binary.
The following table provides a brief reminder of how binary handles fractional components:

<table>
<thead>
<tr>
<th></th>
<th>2^3</th>
<th>2^2</th>
<th>2^1</th>
<th>2^0</th>
<th>2^-1</th>
<th>2^-2</th>
<th>2^-3</th>
</tr>
</thead>
</table>
| ... | 8   | 4   | 2   | 1   | .    | 1/2  | 1/4  | 1/8  | ...
| 0   | 0   | 0   | 0   | 0   | .    | 0    | 0    | 0    |

For example, 100.101₂ would be 4.625₁₀. For repeating decimals, calculating the binary value can be time consuming. However, there is a limit since computers have finite storage.

The next step is to show the value in normalized scientific notation in binary. This means that the number should have a single, non-zero leading digit to the left of the decimal point. For example, 8.125₁₀ is 1000.001₂ (or 1000.001₂ x 2^3) and in binary normalized scientific notation that would be written as 1.000001 x 2^3 (since the decimal point was moved three places to the left). Of course, if the number was 0.125₁₀ the binary would be 0.001₂ (or 0.001₂ x 2^0) and the normalized scientific notation would be 1.0 x 2^-3 (since the decimal point was moved three places to the right). The numbers after the leading 1, not including the leading 1, are stored left-justified in the fraction portion of the word.

The next step is to calculate the biased exponent, which is the exponent from the normalized scientific notation with plus the bias. The bias for the IEEE 754 32-bit floating-point standard is 127₁₀. The result should be converted to a byte (8 bits) and stored in the biased exponent portion of the word.

Note, converting from the IEEE 754 32-bit floating-point representation to the decimal value is done in reverse, however leading 1 must be added back (as it is not stored in the word). Additionally, the bias is subtracted (instead of added).

### 3.3.1.1 IEEE 32-bit Representation Examples

This section presents several examples of encoding and decoding floating-point representation for reference.
Chapter 3.0  ▶ Data Representation

3.3.1.1 Example → 7.75\textsubscript{10}

For example, to find the IEEE 754 32-bit floating-point representation for -7.75\textsubscript{10}:

Example 1:  
-7.75
- determine sign  \(-7.75 \Rightarrow 1\) (since negative)
- convert to binary  
  \[-7.75 = -0.1111\textsubscript{2}\]
- normalized scientific notation  
  \[= 1.1111 \times 2^{-2}\]
- compute biased exponent  
  \[2^{-10} + 127\textsubscript{10} = 129\textsubscript{10}\]
  ° and convert to binary  
  \[= 1000001\textsubscript{2}\]
- write components in binary:
  sign  exponent  mantissa
  1  1000001  1111000000000000000000000
- convert to hex (split into groups of 4)  
  \[11000000111110000000000000000000\]
  \[1100 0000 1111 1000 0000 0000 0000 0000\]
  \[C 0 F 8 0 0 0 0\]
- final result:  \textbf{C0F8 0000}\textsubscript{16}

3.3.1.2 Example → 0.125\textsubscript{10}

For example, to find the IEEE 754 32-bit floating-point representation for -0.125\textsubscript{10}:

Example 2:  
-0.125
- determine sign  \(-0.125 \Rightarrow 1\) (since negative)
- convert to binary  
  \[-0.125 = -0.001\textsubscript{2}\]
- normalized scientific notation  
  \[= 1.0 \times 2^{-3}\]
- compute biased exponent  
  \[-3\textsubscript{10} + 127\textsubscript{10} = 124\textsubscript{10}\]
  ° and convert to binary  
  \[= 01111100\textsubscript{2}\]
- write components in binary:
  sign  exponent  mantissa
  1  01111100  0000000000000000000000000
- convert to hex (split into groups of 4)  
  \[10111110000000000000000000000000\]
  \[1011 1110 0000 0000 0000 0000 0000 0000\]
  \[B E 0 0 0 0 0 0\]
- final result:  \textbf{BE00 0000}\textsubscript{16}
3.3.1.3 Example → $41440000_{16}$

For example, given the IEEE 754 32-bit floating-point representation $41440000_{16}$ find the decimal value:

**Example 3:** $41440000_{16}$
- convert to binary
  
  \[0100 0001 0100 0100 0000 0000 0000 0000_2\]
- split into components
  
  \[0 10000010 1000100000000000000_2\]
- determine exponent $10000010_2 = 130_{10}$
  \[\text{and remove bias} \quad 130_{10} - 127_{10} = 3_{10}\]
- determine sign $0 = \text{positive}$
- write result $+1.10001 \times 2^3 = +1100.01 = +12.25$

3.3.2 IEEE 64-bit Representation

The IEEE 754 64-bit floating-point standard is defined as follows:

<table>
<thead>
<tr>
<th>63</th>
<th>62</th>
<th>52</th>
<th>51</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>s</td>
<td>biased exponent</td>
<td>fraction</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The representation process is the same, however the format allows for an 11-bit biased exponent (which support large and smaller values). The 11-bit biased exponent uses a bias of 1023.
4.0 QtSpim Program Formats

The QtSpim MIPS simulator will be used for programs in this text. The QtSpim simulator has a number of features and requirements for writing MIPS assembly language programs. This includes a properly formatted assembly source file.

A properly formatted assembly source file consists of two main parts; the data section (where data is placed) and the text section (where code is placed). The following sections summarize the formatting requirements and explains each of these sections.

4.1 Assembly Process

The QtSpim effectively assembles the program during the load process. Any major errors in the program format or the instructions will be noted immediately. Assembler errors must be resolved before the program can be successfully executed. Refer to Appendix B regarding the use of QtSpim to load and execute programs.

4.2 Comments

The "#" character represents a comment line. Anything typed after the "#" is considered a comment. Blank lines are accepted.

4.3 Assembler Directives

An assembler directive is a message to the assembler, or the QtSpim simulator, that tells the assembler something it needs to know in order to carry out the assembly process. This includes noting where the data is declared or the code is defined. Assembler directives are not executable statements.

Directives are required for data declarations and to define the start and end of procedures. Assembler directives start with a “.”. For example, “.data” or “.text”.

Additionally, directives are used to declare and defined data. The following sections provide some examples of data declarations using the directives.
4.4 Data Declarations

The data must be declared in the ".data" section. All variables and constants are placed in this section. Variable names must start with a letter followed by letters or numbers (including some special characters such as the ","), and terminated with a ":" (colon). Variable definitions must include the name, the data type, and the initial value for the variable. In the definition, the variable name must be terminated with a ":".

The data type must be preceded with a "." (period). The general format is:

```
<variableName>: .<dataType> <initialValue>
```

Refer to the following sections for a series of examples using various data types.

The supported data types are as follows:

<table>
<thead>
<tr>
<th>Declaration</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>.byte</td>
<td>8-bit variable(s)</td>
</tr>
<tr>
<td>.half</td>
<td>16-bit variable(s)</td>
</tr>
<tr>
<td>.word</td>
<td>32-bit variable(s)</td>
</tr>
<tr>
<td>.ascii</td>
<td>ASCII string</td>
</tr>
<tr>
<td>.asciiz</td>
<td>NULL terminated ASCII string</td>
</tr>
<tr>
<td>.float</td>
<td>32 bit IEEE floating point number</td>
</tr>
<tr>
<td>.double</td>
<td>64 bit IEEE floating point number</td>
</tr>
<tr>
<td>.space &lt;n&gt;</td>
<td>&lt;n&gt; bytes of uninitialized memory</td>
</tr>
</tbody>
</table>

These are the primary assembler directives for data declaration. Other directives are referenced in different sections.

4.4.1 Integer Data Declarations

Integer values are defined with the .word, .half, or .byte directives. Two's compliment is used for the representation of negative values. For more information regarding two's compliment, refer to the Data Representation section.
Chapter 4.0  ▶  QtSpim Program Formats

The following declarations are used to define the integer variables "wVar1" and "wVar2" as 32-bit word values and initialize them to 500,000 and -100,000.

\[
\begin{align*}
\text{wVar1:} & \quad \text{.word} \quad 500000 \\
\text{wVar2:} & \quad \text{.word} \quad -100000 
\end{align*}
\]

The following declarations are used to define the integer variables "hVar1" and "hVar2" as 16-bit word values and initialize them to 5,000 and -3,000.

\[
\begin{align*}
\text{hVar1:} & \quad \text{.half} \quad 5000 \\
\text{hVar2:} & \quad \text{.half} \quad -3000 
\end{align*}
\]

The following declarations are used to define the integer variables "bVar1" and "bVar2" as 8-bit word values and initialize them to 5 and -3.

\[
\begin{align*}
\text{bVar1:} & \quad \text{.byte} \quad 5 \\
\text{bVar2:} & \quad \text{.byte} \quad -3 
\end{align*}
\]

If an variable is initialized to a value that can not be stored in the allocated space, an assembler error will be generated. For example, attempting to set a byte variable to 500 would be illegal and generate an error.

4.4.2 String Data Declarations

Strings are defined with \texttt{.asciiz} or \texttt{.asciiz} directives. Characters are represented using standard ASCII characters. Refer to Appendix D for a copy of the ASCII table for reference.

The C/C++ style new line, "\n", and tab, "\t" tab are supported within in strings.

The following declarations are used to define a string "message" and initialize it to “Hello World”.

\[
\begin{align*}
\text{message:} & \quad \text{.asciiz} \quad "Hello World\n"
\end{align*}
\]

In this example, the string is defined as NULL terminated (i.e., after the new line). The NULL is a non-printable ASCII character and is used to mark the end of the string. The NULL termination is standard and is required by the print string system service (to work correctly).
To define a string with multiple lines, the NULL termination would only be required on the final or last line. For example,

```assembly
message:     .ascii "Line 1: Goodbye World\n"
            .ascii "Line 2: So, long and thanks "
            .ascii "for all the fish.\n"
            .asciiz "Line 3: Game Over.\n"
```

When printed, using the starting address of 'message', everything up-to (but not including) the NULL will be displayed. As such, the declaration using multiple lines is not relevant to the final displayed output.

### 4.4.3 Floating-Point Data Declarations

Floating-point values are defined with the `.float` (32-bit) or `.double` (64-bit) directives. The IEEE floating-point format is used for the internal representation of floating-point values.

The following declarations are used to define the floating-point variables "pi" to a 32-bit floating-point value initialized to 3.14159 and "tao" to a 64-bit floating-point values initialized them to 6.28318.

```assembly
pi:         .float 3.14159
tao:        .double 6.28318
```

For more information regarding the IEEE format, refer to the Data Representation section.

### 4.5 Constants

Constant names must start with a letter followed by letters or numbers including some special characters such as the "_" (underscore). Constant definitions are created with an "=" sign.

For example, to create some constants named TRUE and FALSE and set them to 1 and 0 respectively:

```assembly
TRUE = 1
FALSE = 0
```

Constants are also defined in the data section. The use of all capitals for a constant is a convention and not required by the QtSpim program. The convention helps
programmers more easily distinguish between variables (which can change values) and constants (which can not change values). Additionally, in assembly language constants are not typed (i.e., not predefined to be a specific size such as 8-bits, 16-bits, 32-bits, or 64-bits).

4.6 Program Code

The code must be preceded by the ".text" directive.

In addition, there are some basic requirements for naming a "main" procedure (i.e., the first procedure to be executed). The ".globl name" and ".ent name" directives are required to define the name of the initial or main procedure. Note, the globl spelled incorrectly is the correct directive. Also, the main procedure must start with a label with the procedure name. The main procedure (as all procedures) should be terminated with the ".end <name>" directive.

In the following example, the <name> would be the name of the main procedure, which is “main”.

4.7 Labels

Labels are code locations, typically used as function/procedure name or as the target of a jump. The first use of a label is the main program starting location, which must be named 'main' which is a specific requirement for the QtSpim simulator.

The rules for a label are as follows:

- Must start with a letter
- May be followed by letters, numbers, or an “_” (underscore).
- Must be terminated with a “:” (colon).
- May only be define once.

Some examples of a label include:

    main:
    exitProgram:

Characters in a label are case-sensitive. As such, Loop: and loop: are different labels. This can be very confusing initially, so caution is advised.
4.8 Program Template

The following is a very basic template for QtSpim MIPS programs. This general template will be used for all programs.

```assembly
# Name and assignment number

# ---------------------------------
# Data declarations go in this section.

.data
#     program specific data declarations

# ---------------------------------
# Program code goes in this section.

.text
.globl main
.ent main
main:

# -----
# your program code goes here.

# -----
# Done, terminate program.

    li $v0, 10
    syscall          # all done!
.end main
```

The initial header (”.text”, “.globl main”, “.ent main”, and “main:”) will be the same for all QtSpim programs.

A more complete example, with working code, can be found in Appendix A.
# 5.0 Instruction Set Overview

In assembly-language, instructions are how work is accomplished. In assembly the instructions are simple, single operation commands. In a high-level language, one line might be translated into a series of instructions in assembly-language.

This chapter presents a summary of the basic, most common instructions. The *MIPS Instruction Set* Appendix presents a more comprehensive list of the available instructions.

## 5.1 Pseudo-Instructions vs Bare-Instructions

As part of the MIPS architecture, the assembly language includes a number of pseudo-instructions. A bare-instruction is an instruction that is executed by the CPU. A pseudo-instruction is an instruction that the assembler, or simulator, will recognize but then convert into one or more bare-instructions. This text will focus primarily on the pseudo-instructions.

## 5.2 Notational Conventions

This section summarizes the notation used within this text which is fairly common in the technical literature. In general, an instruction will consist of the instruction or operation itself (i.e., add, sub, mul, etc.) and the *operands*. The operands refer to where the data (to be operated on) is coming from or where the result is to be placed.

The following table summarizes the notational conventions used in the remainder of the document.

<table>
<thead>
<tr>
<th>Operand Notation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Rdest</strong></td>
<td>Destination operand. Must be an integer register. Since it is a destination operand, the contents will be over written with the new result.</td>
</tr>
<tr>
<td><strong>Rsrc</strong></td>
<td>Source operand. Must be an integer register. Register value is unchanged after the instruction.</td>
</tr>
</tbody>
</table>
Chapter 5.0  Instruction Set Overview

<table>
<thead>
<tr>
<th></th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Src</strong></td>
<td>Source operand. Must be an integer register or an integer immediate value. Value is unchanged after the instruction.</td>
</tr>
<tr>
<td><strong>FRdest</strong></td>
<td>Destination operand. Must be a floating-point register. Since it is a destination operand, the contents will be overwritten with the new result.</td>
</tr>
<tr>
<td><strong>FRsrc</strong></td>
<td>Source operand. Must be a floating-point register. Register value is unchanged after the instruction.</td>
</tr>
<tr>
<td><strong>Imm</strong></td>
<td>Immediate value.</td>
</tr>
<tr>
<td><strong>Mem</strong></td>
<td>Memory location. May be a variable name or an indirect reference (i.e., a memory address).</td>
</tr>
</tbody>
</table>

By default, the immediate values are decimal or base-10. Hexadecimal or base-16 immediate values may be used but must be preceded with a 0x to indicate the value is hex. For example, 15\textsubscript{10} could be entered in hex as 0x0F.

Refer to the chapter on Addressing Modes for more information regarding memory locations and indirection.

5.3 Data Movement

CPU computations are typically performed using registers. As such, before computations can be performed, data is typically moved into registers from variables (i.e., memory) and when the computations are completed the data would be moved out of registers into other variables.

5.3.1 Load and Store

To support the loading of data from memory (e.g., variables or arrays) into registers and storing of data in register back to memory, there are a series of load and store instructions. The load and store instructions only move data between register and memory. Another instruction is used to move data between registers (as described in the next section).

There is no load or store instructions that will move a value from a memory location directly to another memory location.
Chapter 5.0 ► Instruction Set Overview

The general forms of the load and store instructions are as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>l&lt;type&gt;</code></td>
<td>Load value from memory location memory into destination register.</td>
</tr>
<tr>
<td><code>li</code></td>
<td>Load specified immediate value into destination register.</td>
</tr>
<tr>
<td><code>la</code></td>
<td>Load address of memory location into destination register.</td>
</tr>
<tr>
<td><code>s&lt;type&gt;</code></td>
<td>Store contents of source register into memory location.</td>
</tr>
</tbody>
</table>

Assuming the following data declarations:

```
num: .word 0
wnum: .word 42
hnum: .half 73
bnum: .byte 7
wans: .word 0
hans: .half 0
bans: .byte 0
```

To perform, the basic operations of:

```
num = 27
wans = wnum
hans = hnum
bans = bnum
```

The following instructions could be used:

```
li  $t0, 27
sw  $t0, num # num = 27
lw  $t0, wnum
sw  $t0, wans # wans = wnum
lh  $t1, hnum
sh  $t1, hans # hans = hnum
```
Chapter 5.0 Instruction Set Overview

\[
\begin{align*}
\text{lb} & \quad \text{$t2, bnum} \\
\text{sb} & \quad \text{$t2, bans} \quad \# \text{ bans = bnum}
\end{align*}
\]

For the halfword and byte instructions, only the lower 16-bits are used.

5.3.2 Move

The various forms of the move instructions are used to move data between registers. Both operands must be registers. The most basic move instruction, move, copies the contents of an integer register into another integer register. Another set of move instructions are used to move the contents of registers into or out of the special registers, $\text{hi}$ and $\text{lo}$.

In addition, different move instructions are required to move values between integer registers and floating point registers (as discussed on the floating-point section).

There is no move instruction that will move a value from a memory location directly to another memory location.

The general forms of the move instructions are as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>move</td>
<td>Rdest, RSrc</td>
</tr>
<tr>
<td>mfhi</td>
<td>Rdest</td>
</tr>
<tr>
<td>mflo</td>
<td>Rdest</td>
</tr>
<tr>
<td>mthi</td>
<td>Rdest</td>
</tr>
<tr>
<td>mtlo</td>
<td>Rdest</td>
</tr>
</tbody>
</table>
For example, the following instructions;

\[
\begin{align*}
\text{li} & \quad \$t0, \ 42 \\
\text{move} & \quad \$t1, \ $t0
\end{align*}
\]

will move the contents of register \(\$t0\), 42 in this example, into the \(\$t1\) register.

The \textit{mfhi}, \textit{mflo}, \textit{mtho}, and \textit{mtlo} instructions are required only when performing 64-bit integer multiply and divide operations.

The floating point section will include examples for moving data between integer and floating point registers.

### 5.4 Integer Arithmetic Operations

The arithmetic operations include addition, subtraction, multiplication, division, remainder (remainder after division), logical AND, and logical OR. The general format for these basic instructions is as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
</table>
| add Rdest, Rsr, Src | Signed addition  
Rdest = Rsr + Src or Imm |
| addu Rdest, Rsr, Src | Unsigned addition  
Rdest = Rsr + Src or Imm |
| sub Rdest, Rsr, Src | Signed subtraction  
Rdest = Rsr – Src or Imm |
| subu Rdest, Rsr, Src | Unsigned subtraction  
Rdest = Rsr – Src or Imm |
| mul Rdest, Rsr, Src | Signed multiply with no overflow  
Rdest = Rsr * Src or Imm |
| mulo Rdest, Rsr, Src | Signed multiply with overflow  
Rdest = Rsr * Src or Imm |
| mulou Rdest, Rsr, Src | Unsigned multiply with overflow  
Rdest = Rsr * Src or Imm |
| mult Rsr1, Rsr2 | Signed 64-bit multiply  
\(\$hi/\$lo = \text{Rs}r1 * \text{Rs}r2\) |
Chapter 5.0  Instruction Set Overview

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Format</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>multu</td>
<td>Rsrc1, Rsrc1</td>
<td>Unsigned 64-bit multiply</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$hi$/flo = Rsrc1 * Rsrc2</td>
</tr>
<tr>
<td>div</td>
<td>Rdest, Rsrc, Src</td>
<td>Signed divide</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest = Rsrc / Src or Imm</td>
</tr>
<tr>
<td>divu</td>
<td>Rdest, Rsrc, Src</td>
<td>Unsigned divide</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest = Rsrc / Src or Imm</td>
</tr>
<tr>
<td>div</td>
<td>Rsrc1, Rsrc1</td>
<td>Signed divide with remainder</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$flo = Rsrc1 / Rsrc2</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$hi = Rsrc1 % Rsrc2</td>
</tr>
<tr>
<td>divu</td>
<td>Rsrc1, Rsrc1</td>
<td>Unsigned divide with remainder</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$flo = Rsrc1 / Rsrc2</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$hi = Rsrc1 % Rsrc2</td>
</tr>
<tr>
<td>rem</td>
<td>Rdest, Rsrc, Src</td>
<td>Signed remainder</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest = Rsrc % Src or Imm</td>
</tr>
<tr>
<td>remu</td>
<td>Rdest, Rsrc, Src</td>
<td>Unsigned remainder</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest = Rsrc % Src or Imm</td>
</tr>
<tr>
<td>abs</td>
<td>Rdest, Rsrc</td>
<td>Absolute value</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest =</td>
</tr>
<tr>
<td>neg</td>
<td>Rdest, Rsrc</td>
<td>Signed negation</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest = - Rsrc</td>
</tr>
<tr>
<td>negu</td>
<td>Rdest, Rsrc</td>
<td>Unsigned negation</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Rdest = - Rsrc</td>
</tr>
</tbody>
</table>

These instructions operate on 32-bit registers (even if byte or halfword values are placed in the registers).

Assuming the following data declarations:

```plaintext
wnum1:    .word    651
wnum2:    .word    42
wans1:    .word    0
wans2:    .word    0
wans3:    .word    0
hnum1:    .half    73
hnum2:    .half    15
```
To perform, the basic operations of:

\[
\begin{align*}
\text{wans1} & = \text{wnum1} + \text{wnum2} \\
\text{wans2} & = \text{wnum1} \times \text{wnum2} \\
\text{wans3} & = \text{wnum1} \% \text{wnum2} \\
\text{hans} & = \text{hnum1} \times \text{hnum2} \\
\text{bans} & = \text{bnum1} / \text{bnum2}
\end{align*}
\]

The following instructions could be used:

\[
\begin{align*}
lw & \quad \$t0, \text{wnum1} \\
lw & \quad \$t1, \text{wnum2} \\
\text{add} & \quad \$t2, \$t0, \$t1 \\
\text{sw} & \quad \$t2, \text{wans1} & \# \text{wans1} = \text{wnum1} + \text{wnum2} \\
lw & \quad \$t0, \text{wnum1} \\
lw & \quad \$t1, \text{wnum2} \\
\text{mul} & \quad \$t2, \$t0, \$t1 \\
\text{sw} & \quad \$t2, \text{wans2} & \# \text{wans2} = \text{wnum1} \times \text{wnum2} \\
lw & \quad \$t0, \text{wnum1} \\
lw & \quad \$t1, \text{wnum2} \\
\text{rem} & \quad \$t2, \$t0, \$t1 \\
\text{sw} & \quad \$t2, \text{wans3} & \# \text{wans} = \text{wnum1} \% \text{wnum2} \\
lh & \quad \$t0, \text{hnum1} \\
lh & \quad \$t1, \text{hnum2} \\
\text{mul} & \quad \$t2, \$t0, \$t1 \\
\text{sh} & \quad \$t2, \text{hans} & \# \text{hans} = \text{hnum1} \times \text{hnum2} \\
lb & \quad \$t0, \text{bnum1} \\
lb & \quad \$t1, \text{bnum2} \\
\text{div} & \quad \$t2, \$t0, \$t1 \\
\text{sb} & \quad \$t2, \text{bans} & \# \text{bans} = \text{bnum1} / \text{bnum2}
\end{align*}
\]
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For the halfword load or store instructions, only the lower 16-bits are used. For the byte instructions, only the lower 8-bits are used.

5.4.1 Example Program, Integer Arithmetic

The following is an example program to compute the volume and surface area of a rectangular parallelepiped.

The formulas for the volume and surface area are as follows:

\[
\begin{align*}
\text{volume} &= aSide \times bSide \times cSide \\
\text{surfaceArea} &= 2 \left( aSide \times bSide + aSide \times cSide + bSide \times cSide \right)
\end{align*}
\]

This example main initializes the \(a, b,\) and \(c\) sides to arbitrary integer values.

```plaintext
# Example to compute the volume and surface area of a rectangular parallelepiped.

# ----------------------------------------
# Data Declarations
.data

aSide: .word 73
bSide: .word 14
cSide: .word 16

volume: .word 0
surfaceArea: .word 0

# ----------------------------------------
# Text/code section

.text
.globl main
main:

# -----
```

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# Load variables into registers.

lw  $t0, aSide
lw  $t1, bSide
lw  $t2, cSide

# ----
# Find volume of a rectangular parallelepiped.
# volume = aSide * bSide * cSide

mul  $t3, $t0, $t1
mul  $t4, $t3, $t2
sw  $t4, volume

# ----
# Find surface area of a rectangular parallelepiped.
# surfaceArea = 2*(aSide*bSide+aSide*cSide+bSide*cSide)

mul  $t3, $t0, $t1  # aSide * bSide
mul  $t4, $t0, $t2  # aSide * cSide
mul  $t5, $t1, $t2  # bSide * cSide
add  $t6, $t3, $t4
add  $t7, $t6, $t5
mul  $t7, $t7, 2
sw  $t7, surfaceArea

# ----
# Done, terminate program.

li  $v0, 10  # call code for terminate
syscall  # system call

.end main

Refer to the system services section for information on displaying the final results to the console.

5.5 Logical Operations

The logical operations include logical AND, and logical OR, shift and rotate instructions. The general format for these instructions is as follows:
<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
</table>
| **and** Rdest, Rsrc, Src | Logical AND  
Rdest = Rsrc & Src or Imm |
| **nor** Rdest, Rsrc, Src | Logical NOR  
Rdest = Rsrc ↓ Src or Imm |
| **not** Rdest, Rsrc, Src | Logical NOT  
Rdest = Rsrc ¬ Src or Imm |
| **or** Rdest, Rsrc, Src | Logical OR  
Rdest = Rsrc | Src or Imm |
| **rol** Rdest, Rsrc, Src | Rotate left  
Rdest = Rsrc rotated left Src  or Imm places |
| **ror** Rdest, Rsrc, Src | Rotate right  
Rdest = Rsrc rotated right Src  or Imm places |
| **sll** Rdest, Rsrc, Src | Shift left logical  
Rdest = Rsrc shift left logical  Src or Imm places |
| **sra** Rdest, Rsrc, Src | Shift right arithmetic  
Rdest = Rsrc shift right arithmetic Src or  Imm places |
| **srl** Rdest, Rsrc, Src | Shift right logical  
Rdest = Rsrc shift right logical  Src or Imm places |
| **xor** Rdest, Rsrc, Src | Logical XOR  
Rdest = Rsrc ^ Src or Imm |

The & refers to the logical AND operation, the | refers to the logical OR operation, and the ^ refers to the logical XOR operation as per C/C++ conventions. The ↓ refers to the logical NOR operation and the ¬ refers to the logical NOT operation.

These instructions operate on 32-bit registers (even if byte or halfword values are placed in the registers).
Assuming the following data declarations:

\[
\begin{align*}
wnum1 & : \text{.word} \quad 0x000000ff \\
wnum2 & : \text{.word} \quad 0x0000ff00 \\
wans1 & : \text{.word} \quad 0 \\
wans2 & : \text{.word} \quad 0 \\
wans3 & : \text{.word} \quad 0
\end{align*}
\]

To perform, the basic operations of:

\[
\begin{align*}
wans1 & = \text{wnum1} \land \text{wnum2} \\
wans2 & = \text{wnum1} \lor \text{wnum2} \\
wans3 & = \neg \text{wnum2}
\end{align*}
\]

The following instructions

\[
\begin{align*}
lw & \quad \$t0, \text{wnum1} \\
lw & \quad \$t1, \text{wnum2} \\
\text{and} & \quad \$t2, \$t0, \$t1 \\
\text{sw} & \quad \$t2, \text{wans1} & \quad \# \text{wans1} & = \text{wnum1} \land \text{wnum2} \\
lw & \quad \$t0, \text{wnum1} \\
lw & \quad \$t1, \text{wnum2} \\
\text{or} & \quad \$t2, \$t0, \$t1 \\
\text{sw} & \quad \$t2, \text{wans2} & \quad \# \text{wans2} & = \text{wnum1} \lor \text{wnum2} \\
lw & \quad \$t0, \text{wnum1} \\
lw & \quad \$t1, \text{wnum2} \\
\text{not} & \quad \$t2, \$t0, \$t1 \\
\text{sw} & \quad \$t2, \text{wans3} & \quad \# \text{wans3} & = \text{wnum1} \lor \text{wnum2}
\end{align*}
\]

For halfword load or store instructions, only the lower 16-bits are used. For the byte instructions, only the lower 8-bits are used.

### 5.5.1 Shift Operations

The shift operations shift or move bits within a register. Two typical reasons for shifting bits include isolating a subset of the bits within an operand for some specific purpose or possibly for performing multiplication or division by powers of two. The two shift operations are a logical shift and an arithmetic shift.
5.0 Instruction Set Overview

5.5.1.1 Logical Shift

The logical shift is a bitwise operation that shifts all the bits of its source register by the specified number of bits places the result into the destination register. The bits can be shifted left or right as needed. Every bit in the source operand is moved the specified number of bit positions and the newly vacant bit-positions are filled in with zeros. The following diagram shows how the right and left shift operations work for byte sized operands.

The logical shift treats the operand as a sequence of bits rather than as a number.

The shift instructions may be used to perform unsigned integer multiplication and division operations for powers of 2. Powers of two would be 2, 4, 8, etc. up to the limit of the operand size (32-bits for register operands).

In the examples below, 23 is divided by 2 by performing a shift right logical one bit. The resulting 11 is shown in binary. Next, 13 is multiplied by 4 by performing a shift left logical two bits. The resulting 52 is shown in binary.

As can be seen in the examples, a 0 was entered in the newly vacated bit locations on either the right or left (depending on the operation).
5.5.1.2 Arithmetic Shift

The arithmetic shift right is also a bitwise operation that shifts all the bits of its source register by the specified number of bits places the result into the destination register. Every bit in the source operand is moved the specified number of bit positions, and the newly vacant bit-positions on the left are filled in. The original leftmost bit (the sign bit) is replicated to fill in all the vacant positions. This is referred to as sign extension. The following diagram shows how the shift right arithmetic operations works for a byte sized operand.

![Shift Right Arithmetic Diagram]

The arithmetic shift treats the operand as a signed number and extends the sign which would be negative in this example.

However, the arithmetic shift rounds up and the standard divide instruction truncates. As such, the arithmetic shift is not typically used to replace the signed divide instruction.

5.5.1.3 Shift Operations, Examples

This section provides a series of examples using the logical shift operations.

Assuming the following data declarations:

```plaintext
data1:    .word       0x000000ff
result1: .word       0
result2: .word       0
```

To perform, the basic operations of:

```plaintext
result1 = wnum1, rotated left 1 bit
result2 = wnum1, rotated right 1 bit
```
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The following instructions

\[
\begin{align*}
&\text{lw} \quad $t0, \text{wnum1} \\
&\text{lw} \quad $t1, \text{wnum2} \\
&\text{rol} \quad $t2, $t0, $t1 \\
&\text{sw} \quad $t2, \text{wans3} \quad \# \text{wans3 = wnum1, rotated left 1 bit} \\
&\text{lw} \quad $t0, \text{wnum1} \\
&\text{lw} \quad $t1, \text{wnum2} \\
&\text{ror} \quad $t2, $t0, $t1 \\
&\text{sw} \quad $t2, \text{wans4} \quad \# \text{wans3 = wnum1, rotated right 1 bit}
\end{align*}
\]

For halfword instructions, only the lower 16-bits are used. For the byte instructions, only the lower 8-bits are used.

To perform the operation, \textbf{value} \times 8, it would be possible to shift the number in the variable one bit for each power of two, which would be three bits in this example.

Assuming the following data declarations:

\[
\begin{align*}
\text{value:} \quad \text{.word} \quad 17 \\
\text{answer:} \quad \text{.word} \quad 0
\end{align*}
\]

The following instructions could be used to multiple value by 8.

\[
\begin{align*}
&\text{lw} \quad $t0, \text{value} \\
&\text{shl} \quad $t1, $t0, 3 \\
&\text{sw} \quad $t1, \text{answer} \quad \# \text{answer = wnum1 * 8}
\end{align*}
\]

The final value in answer would be 17 \times 8 or 136.

In the context of an encoded MIPS instruction, the upper 6-bits of a 32-bit word represent the OP or operation field. If a program was analyzing code, it might be desirable to isolate these bits for comparison. One way this can be performed is to use a logical right shift to move the upper six bits into the position of the lower 6-bits.

The instruction:

\[
\begin{align*}
\text{add} \quad $t1, $t1, 1
\end{align*}
\]

will be translated by the assembler into the hex value of \textbf{0x2129001}. 

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Assuming the following data declarations:

```assembly
inst1: .word 0x2129001
inst1Op1: .word 0
```

To mask out the OP field (upper 6-bits) for `inst1` and place it in the variable `instOp1` (lower 6-bits), the following instructions could be used:

```assembly
lw  $t0, inst1
shr $t1, $t0, 26
sw  $t1, instOp1
```

This can be done in one step since the logical shift will insert all 0's into the newly vacated bit locations.

## 5.6 Control Instructions

Program control refers to basic programming structures such as IF statements and looping. All of the high-level language control structures must be performed with the limited assembly-language control structures. For example, an IF-THEN-ELSE statement does not exist as the assembly-language level. Assembly-language provided an unconditional branch (or jump) and a conditional branch or an IF statement that will jump to a target label or not jump.

The control instructions refer to unconditional and conditional branching. Branching is required for basic conditional statements (i.e., IF statements) and looping.

### 5.6.1 Unconditional Control Instructions

The unconditional instruction provides an unconditional jump to a specific location.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>j &lt;label&gt;</td>
<td>Unconditionally branch to the specified label.</td>
</tr>
</tbody>
</table>

The “b” (branch) may be used instead of the “j” (jump). Both are encoded as the same instruction (an unconditional jump). An error is generated by QtSpim if the label is not defined.
5.6.2 Conditional Control Instructions

The conditional instruction provides a conditional jump based on a comparison. This is a basic IF statement.

The conditional control instructions include the standard set; branch equal, branch not equal, branch less than, branch less than or equal, branch greater than, and branch greater than or equal.

The general format for these basic instructions is as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>beq &lt;Rsrc&gt;, &lt;Src&gt;, &lt;label&gt;</td>
<td>Branch to label if &lt;Rscr&gt; and &lt;Scr&gt; are equal</td>
</tr>
<tr>
<td>bne &lt;Rsrc&gt;, &lt;Src&gt;, &lt;label&gt;</td>
<td>Branch to label if &lt;Rscr&gt; and &lt;Scr&gt; are not equal</td>
</tr>
<tr>
<td>blt &lt;Rsrc&gt;, &lt;Src&gt;, &lt;label&gt;</td>
<td>Branch to label if &lt;Rscr&gt; is less than &lt;Scr&gt;</td>
</tr>
<tr>
<td>ble &lt;Rsrc&gt;, &lt;Src&gt;, &lt;label&gt;</td>
<td>Branch to label if &lt;Rscr&gt; is less than or equal to &lt;Scr&gt;</td>
</tr>
<tr>
<td>bgt &lt;Rsrc&gt;, &lt;Src&gt;, &lt;label&gt;</td>
<td>Branch to label if &lt;Rscr&gt; is greater than &lt;Scr&gt;</td>
</tr>
<tr>
<td>bge &lt;Rsrc&gt;, &lt;Src&gt;, &lt;label&gt;</td>
<td>Branch to label if &lt;Rscr&gt; is greater than or equal to &lt;Scr&gt;</td>
</tr>
</tbody>
</table>

These instructions operate on 32-bit registers (even if byte or halfword values are placed in the registers).

5.6.3 Example Program, Sum of Squares

The following is an example program to find the sum of squares from 1 to \( n \). For example, the sum of squares for 10 is as follows:

\[
1^2 + 2^2 + \cdots + 10^2 = 385
\]
This example main initializes the $n$ to arbitrary to 10 to match the example.

```assembly
# Example program to compute the sum of squares.
# __________________________________________________________
# Data Declarations

.data
n: .word 10
sumOfSquares: .word 0

# __________________________________________________________
# text/code section

.text
.globl main
main:

# ----- # Compute sum of squares from 1 to n.
    lw  $t0, n         #
    li  $t1, 1        # loop index (1 to n)
    li  $t2, 0        # sum

sumLoop:
    mul $t3, $t1, $t1   # index^2
    add $t2, $t2, $t3

    add $t1, $t1, 1
    ble $t1, $t0, sumLoop

    sw  $t2, sumOfSquares

# ----- # Done, terminate program.
    li  $v0, 10        # call code for terminate
    syscall           # system call
.end main
```
Refer to the system services section for information on displaying the final results to the console.

5.7 Floating-Point Instructions

This section presents a summary of the basic, most common floating-point arithmetic instructions. The *MIPS Instruction Set* Appendix presents a more comprehensive list of the available instructions.

5.7.1 Floating-Point Register Usage

The floating-point instructions are similar to the integer instructions, however the floating-point register must be used with the floating-point instructions. Specifically, this means the architecture does not support the use of integer registers for any floating point arithmetic operations.

When single-precision (32-bit) floating-point operation is performed, the specified 32-bit floating-point register is used. When a double-precision (64-bit) floating-point operation is performed, two 32-bit floating-point registers are used; the specified 32-bit floating-point register and the next numerically sequential register is used by the instruction. For example, a double-precision operation using $f12$ will use automatically $f12$ and $f13$.

5.7.2 Floating-Point Data Movement

Floating-point CPU computations are typically performed using floating-point registers. As such, before computations can be performed, data is typically moved into the floating-point registers from other floating-point registers or variables (i.e., memory). When a computation is completed the data might be moved out of the floating-point register into a variable or another floating-point register.

To support the loading of data from memory into floating-point registers and storing of data in floating-point registers to memory, there are a series of specialized load and store instructions. The basic format is the same as the integer operations, however the type is either “.s” for single-precision 32-bit IEEE floating-point representation or “.d” for double-precision 64-bit IEEE floating-point representation. More information regarding the representations can be found in Chapter 2, *Data Representation*. 
The general forms of the floating-point load and store instructions are as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>l.&lt;type&gt; FRdest, mem</td>
<td>Load value from memory location memory into destination register.</td>
</tr>
<tr>
<td>s.&lt;type&gt; FRsrc, mem</td>
<td>Store contents of source register into memory location.</td>
</tr>
<tr>
<td>mov.&lt;type&gt; Frdest, FRsrc</td>
<td>Copy the contents of source register into the destination register.</td>
</tr>
</tbody>
</table>

In this case, the floating-point types are “.s” for single-precision and “.d” for double-precision.

Assuming the following data declarations:

```plaintext
fnum1: .float 3.14  
fnum2: .float 0.0  
dnum1: .double 6.28  
dnum2: .double 0.0
```

The “.float” directive declares a variable as a 32-bit floating-point value and the “.double” declares a variable as a 64-bit floating-point variable.

To perform, the basic operations of:

```plaintext
fnum2 = fnum1  
dnum2 = dnum1
```

The following instructions:

```plaintext
l.s $f6, fnum1  
s.s $f6, fnum2  # fnum2 = fnum1

l.d $f6, dnum1  
mov.d $f8, $f6  # unnecessary use of mov  
    # just as an example

s.d $f8, dnum2  # dnum2 = dnum1
```

The two double-precision operation (l.d and mov.d) reference registers $f6 and $f8 but use registers $f6/$f7 and $f8/$f9 to hold the 64-bit values.
5.7.3 Integer / Floating-Point Register Data Movement

The arithmetic instructions require either floating point registers or integer registers and will not allow a combination. In order to move data between integer and floating point registers, special instructions are required. As noted in Chapter 2, *MIPS Architecture Overview*, the floating point operations are performed in a floating-point co-processor.

The general form of the integer and floating-point data movement instructions are as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>mfc1</td>
<td>Rdest, FRsrc</td>
</tr>
<tr>
<td>mfc1.d</td>
<td>Rdest, FRsrc</td>
</tr>
<tr>
<td>mtc1</td>
<td>Rsrcl, FRdest</td>
</tr>
<tr>
<td>mtc1.d</td>
<td>Rsrcl, FRdest</td>
</tr>
</tbody>
</table>

Copy the contents from co-processor 1 (FPU) float registers FRsrc and FRsrc+1 into integer registers Rdest and Rdest+1.

Note, the above instructions use a 1 (number one) and not a lower-case letter L.

For example, assuming an integer value is in integer register $s0$, to copy the value into floating-point register $f12$, the following instruction could be used.

```
mtc1 $s0, $f12
```

To copy the contents of $f12$, into an integer register $t1$, the following instruction could be used.

```
mfc1 $t1, $f12
```

The value copied has not be converted into a different representation.
In this example, the integer value in $s0 that was copied into $f12 is still represented as an integer in two's compliment. As such, the value in $f12 is not ready for any floating-point arithmetic operations. The representation of the value must be converted (see next section).

### 5.7.4 Integer / Floating-Point Conversion Instructions

When data is moved between integer and floating-point registers, the data representation must be addressed. For example, when moving an integer value from an integer register into a floating-point register, the data is still represented as an integer value in two’s compliment. Floating-point operations require an appropriate floating point representation (32-bit or 64-bit). When data is moved between integer and floating-point registers, a data conversion would typically be required.

The general format for the conversion instructions is as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>cvt.d.s FRdest, FRsrc</td>
<td>Convert the 32-bit floating point value in register FRsrc into a double precision value and put it in register FRdest.</td>
</tr>
<tr>
<td>cvt.d.w FRdest, FRsrc</td>
<td>Convert the 32-bit integer in register FRsrc into a double precision value and put it in register FRdest.</td>
</tr>
<tr>
<td>cvt.s.d FRdest, FRsrc</td>
<td>Convert the 64-bit floating point value in register FRsrc into a 32-bit floating-point value and put it in register FRdest.</td>
</tr>
<tr>
<td>cvt.s.w FRdest, FRsrc</td>
<td>Convert the 32-bit integer in register FRsrc into a 32-bit floating-point value and put it in register FRdest.</td>
</tr>
<tr>
<td>cvt.w.d FRdest, FRsrc</td>
<td>Convert the 64-bit floating-point value in register FRsrc into a 32-bit integer value and put it in register FRdest.</td>
</tr>
</tbody>
</table>
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<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>cvt.w.s</code></td>
<td>FRdest, FRsrc</td>
</tr>
<tr>
<td></td>
<td>Convert the 32-bit floating-point value in register FRsrc into a 32-bit integer value and put it in register FRdest.</td>
</tr>
</tbody>
</table>

Assuming the following data declarations:

```
    iNum: .word       42
    fNum: .float      0.0
```

To convert the integer value in variable `iNum1` and place it as a 32-bit floating-point value in variable `fNum1`, the following instructions could be used:

```
lw    $t0, iNum
mtc1  $t0, $f6
cvt.s.w $f8, $f6
s.s   $f8, fNum
```

This code fragment loads the integer value in variable `iNum` into `$t0`, and then copies the value into `$f6`. The integer value in `$f6` is converted into a 32-bit floating-point value and placed in `$f8`. The 32-bit floating-point value is then copied into the `fNum1` variable. The conversion instruction could have over-written the `$f6` register.

Assuming the following data declarations:

```
    pi:  .double   3.14
    intPi: .word   0
```

To convert the 64-bit floating-point value in variable `pi` and place it as a 32-bit integer value in variable `intPi`, the following instructions could be used:

```
l.d    $f10, pi
cvt.w.d $f12, $f10
mfc1   $t1, $f12
sw     $t1, intPi
```

This code fragment initially loads the 64-bit floating-point value into `$f10`. The 64-bit floating-point value in `$f10` is converted into a 32-bit integer value and placed in `$f12`. The integer value in `$f12` is copied into `$t1` and then copied into the variable `intPi`. Since conversion from floating-point truncates, the final value in `intPi` is 3.
5.7.5 Floating-Point Arithmetic Operations

The arithmetic operations include addition, subtraction, multiplication, division, remainder (remainder after division), logical AND, and logical OR. The general format for these basic instructions is as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>add&lt;type&gt;</td>
<td>FRdest = FRsrc + FRsrc</td>
</tr>
<tr>
<td>sub&lt;type&gt;</td>
<td>FRdest = FRsrc - FRsrc</td>
</tr>
<tr>
<td>mul&lt;type&gt;</td>
<td>FRdest = FRsrc * FRsrc</td>
</tr>
<tr>
<td>div&lt;type&gt;</td>
<td>FRdest = FRsrc / FRsrc</td>
</tr>
<tr>
<td>rem&lt;type&gt;</td>
<td>FRdest = FRsrc % FRsrc</td>
</tr>
</tbody>
</table>

Assuming the following data declarations:

```
.fnum1: .float 6.28318
.fnum2: .float 3.14159
.fans1: .float 0.0
.fans2: .float 0.0
.dnum1: .double 42.3
.dnum2: .double 73.6
.dans1: .double 0.0
.dans2: .double 0.0
```

To perform, the basic operations of:

```
fans1 = fnum1 + fnum2
fans2 = fnum1 * fnum2
dans1 = dnum1 - dnum2
dans2 = dnum1 / dnum2
```

The following instructions:

```
1.s $f4, fnum1
1.s $f6, fnum2
add.s $f8, $f4, $f6
s.s $f8, fans1  # fans1 = fnum1 + fnum2
```
Chapter 5.0 Instruction Set Overview

mul.s $f10, $f4, $f6
s.s $f10, fans2       # fans2 = fnum1 * fnum2
l.d $f4, fnum1       
l.d $f6, fnum2       
sub.d $f8, $f4, $f6  
s.d $f8, fans1       # dans1 = dnum1 - dnum2
div.d $f10, $f4, $f6  
s.d $f10, fans2       # dans2 = dnum1 / dnum2

For the double-precision instructions, the specified register and the next numerically sequential register is used. For example, the l.d instruction sets the $f4 and $f5 32-bit registers with the 64-bit value.

5.7.6 Example Programs

This section provides some example using the floating-point instructions to perform some basic calculations.

5.7.6.1 Example Program, Floating-Point Arithmetic

The following is an example program to compute the surface area and volume of a sphere.

The formulas for the surface area and volume of a sphere are as follows:

\[
surfaceArea = 4.0 \times \pi \times radius^2
\]

\[
volume = \frac{4.0 \times \pi}{3.0} \times radius^3
\]

This example main initializes the radius to arbitrary floating-point value.

    # Example program to calculate the surface area
    # and volume of a sphere given the radius.
    # ---------------------------------------------
# Data Declarations

.data
pi: .float 3.14159
fourPtZero: .float 4.0
threePtZero: .float 3.0
radius: .float 17.25
surfaceArea: .float 0.0
volume: .float 0.0

# text/code section

.text
.globl main
main:

# Compute: (4.0 * pi) which is used for both equations.

l.s $f2, fourPtZero
l.s $f4, pi
mul.s $f4, $f2, $f4 # 4.0 * pi

l.s $f6, radius # radius

# Calculate surface area of a sphere.
# surfaceArea = 4.0 * pi * radius^2

mul.s $f8, $f6, $f6 # radius^2
mul.s $f8, $f4, $f8 # 4.0 * pi * radius^2
s.s $f8, surfaceArea # store final answer

# Calculate volume of a sphere.
# volume = (4.0 * pi / 3.0) * radius^3

l.s $f8, threePtZero
Chapter 5.0  Instruction Set Overview

\[
\begin{align*}
\text{div.s} & \quad \$f2, \$f4, \$f8 \quad \# (4.0 \times \pi / 3.0) \\
\text{mul.s} & \quad \$f10, \$f2, \$f2 \\
\text{mul.s} & \quad \$f10, \$f10, \$f6 \quad \# \text{radius}^3 \\
\text{mul.s} & \quad \$f12, \$f6, \$f10 \quad \# 4.0\pi/3.0\times\text{radius}^3 \\
\text{s.s} & \quad \$f12, \text{volume} \quad \# \text{store final answer}
\end{align*}
\]

# -----
# Done, terminate program.

\[
\begin{align*}
\text{li} & \quad \$v0, 10 \quad \# \text{terminate call code} \\
\text{syscall} & \quad \# \text{system call} \\
\text{.end main} & \quad \# \text{system call}
\end{align*}
\]

Refer to the system services section for information on displaying the final results to the console.

5.7.6.2 Example Program, Integer / Floating-Point Conversion

The following is an example program to sum an array of integer values and compute the average as a floating-point value. This requires conversion of 32-bit integer values into 32-bit floating-point values.

\[
\begin{align*}
\text{# Example program to sum an array of integers} \\
\text{# and compute the float average.}
\end{align*}
\]

\[
\begin{align*}
\text{# -----------------------------------} \\
\text{# Data Declarations}
\end{align*}
\]

\[
\begin{align*}
\text{.data} \\
\text{iArray:} & \quad \text{.word} \quad 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12 \\
\text{length:} & \quad \text{.word} \quad 12 \\
\text{iSum:} & \quad \text{.word} \quad 0 \\
\text{fAve:} & \quad \text{.float} \quad 0.0
\end{align*}
\]
# ---------------# Text/code section
.text
.globl main
main:

# -----# Find the sum of the integer numbers.

la $t0, iArray  # array starting addr
lw $t1, length  # array length
li $t2, 0      # set sum=0

sumLoop:
lw        $t3, ($t0)  # get iArray(n)
add $t2, $t2, $t3  # sum = sum+iArray(n)
addu $t0, $t0, 4  # update iArray addr
sub $t1, $t1, 1
bnez $t1, sumLoop

sw $t2, iSum  # save integer sum

mtc1 $t2, $f6  # move to float reg
cvt.s.w $f6, $f6  # cvt to float format

lw $t1, length
mtc1 $t1, $f8  # move to float reg
cvt.s.w $f8, $f8  # cvt to float format

div.s $f10, $f6, $f8  # sum / length
s.s $f10, fAve

# -----# Done, terminate program.

li $v0, 10     # call code for terminate
syscall        # system call
.end main
Chapter 5.0 ➥ Instruction Set Overview
6.0 Addressing Modes

This chapter provides basic information regarding addressing modes and the associated address manipulations on the MIPS architecture. The addressing modes are the supported methods for specifying the value or address of a data item being accessed (read or written). This might include an actual value, the name of a variable, or the location in an array.

Since the MIPS architecture, as simulated in the QtSpim simulator, is a 32-bit architecture, all addresses are words (32-bits).

6.1 Direct Mode

Direct addressing mode is when the register or memory location contains the actual values.

For example:

```assembly
lw  $t0, var1
lw  $t1, var2
```

Registers and variables $t0, $t1, var1, and var2 are all accessed in direct mode addressing.

6.2 Immediate Mode

Immediate addressing mode is when the actual value is one of the operands.

For example:

```assembly
li  $t0, 57
add $t0, $t0, 57
```

The value 57 is immediate mode addressing. The register $t0 is direct mode addressing.
6.3 Indirection

The ()'s are used to denote an indirect memory access. An indirect memory access means the CPU will read the provided address and then go to that address to access the value located there. This involves more work for the CPU than the previously presented addressing modes (direct and immediate). This is typically how elements are accessed in a list or array. For example, to get a value from a list of longs:

\[
\begin{align*}
&{\text{la}} & &${t0}, \ lst \\
&{\text{lw}} & &{s1}, ({$t0})
\end{align*}
\]

The address, in $t0$, is a word size (32-bits). Memory is byte addressable. As such, if the data items in "lst" (from above) are words, then four add must be added to get the next element.

For example, the instructions:

\[
\begin{align*}
&{\text{add}} & &${t0} \ {t0}, 4 \\
&{\text{lw}} & &{s2}, ({$t0})
\end{align*}
\]

will get the next word value in array (named lst in this example).

A form of displacement addressing is allowed. For example, to get the second item from a list of long sized values:

\[
\begin{align*}
&{\text{la}} & &${t0}, \ lst \\
&{\text{lw}} & &{s1}, 4($t0)
\end{align*}
\]

The "4" is added to the address before the memory access. However, the register is not changed. Thus, the location or address being accessed is displaced or temporarily changed as needed.

6.3.1 Bounds Checking

In a high-level language, the compiler is capable of ensuring that the index for an element in an array is legal and within the boundary of the array being accessed. Thus, the compiler can issue an error message and help identify when and where a program is trying to access beyond the end of an array (e.g., accessing the 110th element of a 100 element array).

This type of bounds checking is not available at the assembly-language level.
If the assembly-language program attempts to access the 110 element of an array, the value at that memory location will be returned with no error. Of course, the value returned is not likely to be useful.

If the memory access attempting to be accessed is outside the general scope of the program, an exception will be generated. An exception is a run-time error. The QtSpim simulator will provide the line where the error occurred. For example, attempting to access a memory location in the reserved section would not be allowed and thus generate an exception. This could easily occur if the programmer uses a register with a data item instead of a correct address.

Additionally, no error is generated when a program attempts to access a word (32-bits) in an array or halfwords (16-bits). In this case two halfwords will be read into the registers and treated as a single value. Of course, the value will not be correct or useful.

### 6.4 Examples

This section provides some example using the addressing modes to access arrays and perform basic calculations.

#### 6.4.1 Example Program, Sum and Average

The following example computes the sum and average for an array integer values. The values are calculated and saved into memory variables.

```assembly
# Example to compute the sum and integer average
# for an array of integer values.

# -----------------------------------------------
# Data Declarations

.data

array: .word 1, 3, 5, 7, 9, 11, 13, 15, 17, 19
      .word 21, 23, 25, 27, 29, 31, 33, 35, 37, 39
      .word 41, 43, 45, 47, 49, 51, 53, 55, 57, 59
length: .word 30

sum:   .word 0
average: .word 0
```
# Basic approach:
# - loop through the array
#   - accessing each value
#   - update sum
# - calculate the average

.text
.globl main
main:

# -----
# Loop through the array to calculate sum

la $t0, array      # array starting address
li $t1, 0          # loop index, i=0
lw $t2, length     # length
li $t3, 0          # initialize sum=0

sumLoop:
    lw $t4, ($t0)    # get array[i]
    add $t3, $t3, $t4  # sum = sum + array[i]
    add $t1, $t1, 1  # i = i+1
    add $t0, $t0, 4  # update array address
    blt $t1, $t2, sumLoop   # if i<length, continue
    sw $t3, sum       # save sum

# -----
# Calculate average
# note, sum and length set in section above.

div $t5, $t3, $t2   # ave = sum / length
sw $t5, average
# Diamond

# Done, terminate program.

```assembly
li $v0, 10                  # call code for terminate
syscall                    # system call

.end main
```

This example program does not display the results to the screen. For information regarding displaying values and strings to output (console), refer to the QtSpim System Services section.

### 6.4.2 Example Program, Median

The following example finds the median for a sorted array of values. In this example, the length is given as always even. As such, the integer median is the integer average for the two middle values. Specifically, the formula for median is:

\[
\text{medianEvenOnly} = \frac{\text{array}[\text{length}/2] + \text{array}[\text{length}/2 - 1]}{2}
\]

The 'length/2' notation refers to using division by two to generate the correct index of the appropriate value from the array. In assembly, we must convert the index into the offset from the base address (i.e., starting address) of the array. Since the data in this example is words (i.e., 4 bytes), it will be necessary to multiply by four to convert the index into an offset. That offset is from the start of the array, so the final address is the array base address plus the offset.

This requires a series of calculations as demonstrated in the following example.

```assembly
# Example to find the median of a sorted
# array of integer values of even length.

# ------------------------------------------
# Data Declarations

.data
array:   .word   1, 3, 5, 7, 9, 11, 13, 15, 17, 19
         .word   21, 23, 25, 27, 29, 31, 33, 35, 37, 39
         .word   41, 43, 45, 47, 49, 51, 53, 55, 57, 59
```
Chapter 6.0  Addressing Modes

```
length: .word 30
median: .word 0

# -------------------------------
# text/code section

# The median for an even length array is defined as:
#     median = ( array[len/2] + array[len/2-1] ) / 2
# Note, the len/2 is the index. Must convert the index
# into the an offset from the base address (of the array.
# Since the data is words (4 bytes), multiple the index
# by four to convert to the offset.

.text
.globl main
main:
    la $t0, array  # starting addr of array
    lw $t1, length  # value of length

    div $t2, $t1, 2  # length / 2
    mul $t3, $t2, 4  # cvt index into offset
    add $t4, $t0, $t3  # add base addr of array

    lw $t5, ($t4)  # get array[len/2]
    sub $t4, $t4, 4  # addr of prev value

    lw $t6, ($t4)  # get array[len/2-1]

    add $t7, $t6, $t5  # a[len/2] + a[len/2-1]
    div $t8, $t7, 2  # / 2

    sw $t8, median  # save median

# -----  
# Done, terminate program.
    li $v0, 10  # call code for terminate
    syscall  # system call
.end main
```
This example program does not display the results to the screen. For information regarding displaying values and strings to output (console), refer to the QtSpim System Services section.

Finding the median for an odd length list is left to the reader as an exercise.
Chapter 6.0  ➕  Addressing Modes
7.0 Stack

In a computer, a stack is a type of data structure where items are added and them removed from the stack in reverse order. That is, the most recently added item is the very first one that is removed. This is often referred to as Last-In, First-Out (LIFO).

A stack is heavily used in programming for the storage of information during procedure or function calls. The following chapter provides information and examples regarding procedure and function calls.

Adding an item to a stack is refer to as a push or push operation. Removing an item from a stack is referred to as a pop or pop operation.

It is generally expected that the reader will be familiar with the general concept of a stack.

7.1 Stack Example

To demonstrate the usage of the stack, given an array, \( a = \{7, 19, 37\} \), consider the operations:

\[
\begin{align*}
\text{push} & \quad a[0] \\
\text{push} & \quad a[1] \\
\text{push} & \quad a[2]
\end{align*}
\]

Followed by the operations:

\[
\begin{align*}
\text{pop} & \quad a[0] \\
\text{pop} & \quad a[1] \\
\text{pop} & \quad a[2]
\end{align*}
\]

The initial push will push the 7, followed by the 29, and finally the 37. Since the stack is last-in, first-out, the first item popped off the stack will be the last item pushed, or 37 in this example. The 37 is placed in the first element of the array (over-writing the 7). As this continues, the order of the array elements is reversed.
Chapter 7.0 ▶ Stack

The following diagram shows the progress and the results.

<table>
<thead>
<tr>
<th>stack</th>
<th>stack</th>
<th>stack</th>
<th>stack</th>
<th>stack</th>
<th>stack</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>19</td>
<td>37</td>
<td>19</td>
<td>19</td>
<td>7</td>
</tr>
<tr>
<td></td>
<td>7</td>
<td></td>
<td>7</td>
<td>7</td>
<td>7</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>empty</td>
</tr>
</tbody>
</table>


a = {7, 19, 37}  a = {7, 19, 37}  a = {7, 19, 37}  a = {37, 19, 37}  a = {37, 19, 37}  a = {37, 19, 7}

The following sections provide more detail regarding the implementation and applicable instructions.

### 7.2 Stack Implementation

The current top of the stack is pointed to by the \$sp register. The stack grows downward in memory and it is generally expected that all items pushed and/or popped should be of word size (32-bit).

There is no push or pop instruction. Instead, you must perform the push and pop operations manually.

While it is possible to push/pop items of various sizes (byte, halfword, etc.) it is not recommended. For such operations, it is recommended to use the entire word (4-bytes).

### 7.3 Push

For example, a push would subtract the \$sp by 4 bytes and then copy the operand to that location (in that order). The instructions to push \$t9 would be implemented as follows:

```
subu $sp, $sp, 4
sw    $t9, ($sp)
```

Which will place the contents of the \$t9 register at the top of the stack.
7.4 Pop

A pop would copy the operand and then add 4 bytes (in that order). To pop $t2, the instructions would be as follows:

\[
\begin{align*}
&\text{lw } \quad t2, (sp) \\
&\text{addu } \quad sp, sp, 4 \\
\end{align*}
\]

Which will place the contents of the $t9 register at the top of the stack.

7.5 Multiple push's/pop's

The preferred method of performing multiple pushes or pops is to perform the $sp adjustment only once. For example, to push registers, $s0, $s1, and $s2:

\[
\begin{align*}
&\text{subu } \quad sp, sp, 12 \\
&\text{sw } \quad s0, (sp) \\
&\text{sw } \quad s1, 4(sp) \\
&\text{sw } \quad s2, 8(sp) \\
\end{align*}
\]

And, the commands to pop registers, $s0, $s1, and $s2 as as follows:

\[
\begin{align*}
&\text{lw } \quad s0, (sp) \\
&\text{lw } \quad s1, 4(sp) \\
&\text{lw } \quad s2, 8(sp) \\
&\text{addu } \quad sp, sp, 12 \\
\end{align*}
\]

By performing the stack adjustment only once, it is more efficient for the architecture to execute.

7.6 Example Program, Stack Usage

The following example uses a stack to reverse the elements in an array. The program will push all elements of the array to the stack and the pop all elements back into the array. This will place the elements back into the array in reverse order based on the basic functionality of the stack.

\[
\begin{align*}
\# \quad \text{Example to reverse values in an array} \\
\# \quad \text{by using the stack.}
\end{align*}
\]
# Data Declarations

.data

array: .word 1, 3, 5, 7, 9, 11, 13, 15, 17, 19
       .word 21, 23, 25, 27, 29, 31, 33, 35, 37, 39
       .word 41, 43, 45, 47, 49, 51, 53, 55, 57, 59
length: .word 30

.Text
.globl main
main:

    # -----
    # Loop to read items from array and push onto stack and place.

    la $t0, array               # array starting address
    li $t1, 0                   # loop index, i=0
    lw $t2, length              # length

    pushLoop:
        lw $t4, ($t0)            # get array[i]
        subu $sp, $sp, 4         # push array[i]
        sw $t4, ($sp)
        add $t1, $t1, 1         # i = i+1
        add $t0, $t0, 4         # update array address
It must be noted that there are easier ways to reverse a set of numbers, but they would not help demonstrate stack operations.
8.0 Procedures/Functions

This chapter provides an overview of using assembly language procedures/functions. In C/C++ a procedure is referred to as a void function. Other languages refer to such functions as procedures. A function returns a single value in a more mathematical sense. C/C++ refers to functions as value returning functions.

With regard to calling a procedure/function, there are two primary activities; linkage and argument transmission. Each are explained in the following sections. Additionally, using procedures/functions in MIPS assembly assembly language requires the use of a series of special purpose registers. These special purpose registers are part of the basic integer register set but have a dedicated purpose based upon standardized and conventional usage.

8.1 MIPS Calling Conventions

When writing MIPS assembly-language procedures, the MIPS standard calling conventions should be utilized. This ensures that the code can more effectively re-used, can interact with other compiler-generated code or mixed-language programs, and utilize high-level language libraries.

The calling conventions address register usage, argument passing and register preservation.

There are two categories of procedures as follows:

- Non-leaf procedures
  - These procedures call other procedures.
- Leaf procedures
  - These procedures do not call other procedures (or themselves).

The standard calling convention specified actions for the caller (routine that is calling) and the callee (routine that is being called). The specifics requirements for each are detailed in the following sections.
8.2 Procedure Format

The basic format for a procedure declaration, uses the a global declaration directive (".globl <procName>"), an entry point directive (".ent <procName>"), and an entry label for the procedure. Generally, a procedure declaration is terminated with an end directive (".end <procName>"). The general syntax is as follows:

```
.globl procedureName
.ent procedureName
procedureName:

# code goes here

.end procedureName
```

The use of the ".end <procName>" directive is optional in the QtSpim simulator.

8.3 Caller Conventions

The calling convention addresses specific requirements for the caller or routine that is calling a procedure.

- The calling procedures is expected to save any non-preserved registers ($a0 - $a3, $t0 - $t9, $v0, $v1, $f0 - $f10 and $f16 - $f18) that are required after the call is completed.
- The calling procedure should pass all arguments.
  - The first argument is passed in either $a0 or $f12 ($a0 if integer or $f12 if float single or double precision).
  - The second argument is passed in either $a1 or $f14 ($a1 if integer or $f14 if float single or double precision).
  - The third argument is passed in $a2 (integer only).
  - If the third argument is float, it must be passed on the stack.
  - The fourth argument is passed in $a3 (integer only).
  - If the fourth argument is float, it must be passed on the stack.

Remaining arguments are passed on the stack. Arguments on the stack should be placed on the stack in reverse order. Call by reference arguments load address (la instruction) and call by value load the value.
Calling procedure should use the "jal <proc>" instruction.

Upon completion of the procedure, the caller procedure must restore any saved non-preserved registers and adjust the stack point ($sp) as necessary if any arguments were passed on the stack.

Note, for floating-point arguments appearing in registers you must allocate a pair of registers (even if it's a single precision argument) that start with an even register.

### 8.4 Linkage

The term *linkage* refers to the basic process of getting to a procedure and getting back to the correct location in the calling routine. This does not include argument transmission, which is addressed in the next section.

The basic linkage operation uses the jal and jr instructions. Both instructions utilize the $ra register. This register is set to the return address as part of the procedure call.

The call to a procedure/function requires the procedure/function name, generically labeled as <procName>, as follows:

```
jal    <procName>
```

The jal, or jump and link, instruction, will copy the $pc into the $ra register and jump to the procedure <procName>. Recall that the $pc register points to the next instruction to be executed. That will be the instruction immediately after the call, which is the correct place to return to when the procedure/function has completed.

If the procedure/function does not call any other procedures/functions, nothing additional is required with regard to the $ra register.

A procedure that does not call another procedure is referred to as a "leaf procedure". A procedure that calls another procedure is referred to as a "non-leaf procedure".

The return from procedure is as follows:

```
jr      $ra
```

If the procedure/function calls yet another procedure/function, the $ra must be preserved. Since $ra contains the return address, it will be changed when the procedure/function calls the next procedure/function. As such, it must be saved and restored from the stack in the calling procedure. This is typically performed only once at the beginning and then at the end of the procedure (for non-leaf procedures).
Chapter 8.0  Procedures/Functions

Refer to the example programs for a more detailed series of examples that demonstrate
the linkage.

8.5 Argument Transmission

Based on the context, parameters may be transmitted to procedures/functions as either
values or addresses. These basic approaches are implemented in high-level languages.
The basic argument transmission is accomplished via a combination of registers and the
stack.

8.5.1 Call-by-Value

Call-by-value involves passing a copy of the information being passed to the procedure
or function. As such, the original value can not be altered.

8.5.2 Call-by-Reference

Call-by-reference involves passing the address of the variables. Call-by-reference is
used when passing arrays or when passing variables that will be altered or set by the
procedure or function.

8.5.3 Argument Transmission Conventions

The basic argument transmission is accomplished via a combination of registers and the
stack.

Integer arguments can be passed in registers $a0, a1, a2, and $a3 and floating-point
values passed in $f12 and $f14 (single or double precision floating point).

- The first argument is passed in either $a0 or $f12 ($a0 if integer or $f12 if float
  single or double precision).
- The second argument is passed in either $a1 or $f14 ($a1 if integer or $f14 if
  float single or double precision).
- The third argument is passed in $a2 (integer only).
- If the third argument is float, it must be passed on the stack.
- The fourth argument is passed in $a3 (integer only).
- If the fourth argument is float, it must be passed on the stack.
If the first argument is integer, $a0$ is used and $f12$ should not be used at all. If the first argument is floating-point value, $f12$ is used and $a0$ is not used at all. Any additional arguments are passed on the stack. The following table shows the argument order and register allocation.

<table>
<thead>
<tr>
<th></th>
<th>1st</th>
<th>2nd</th>
<th>3rd</th>
<th>4th</th>
<th>5th</th>
<th>Nth</th>
</tr>
</thead>
<tbody>
<tr>
<td>integer</td>
<td>$a0$</td>
<td>$a1$</td>
<td>$a2$</td>
<td>$a3$</td>
<td>stack</td>
<td>stack</td>
</tr>
<tr>
<td></td>
<td>or</td>
<td>or</td>
<td>or</td>
<td>or</td>
<td></td>
<td></td>
</tr>
<tr>
<td>floating-point value</td>
<td>$f12$</td>
<td>$f14$</td>
<td>stack</td>
<td>stack</td>
<td>stack</td>
<td>stack</td>
</tr>
</tbody>
</table>

Recall that addresses are integers, even when pointing to floating-point values. As such, addresses are passed in integer registers.

### 8.6 Function Results

A function is expected to return a result (i.e., value returning function).

Integer registers $v0$ or $v1/v0$ are used to return integers values from function/procedure calls. Floating point registers $f0$ and $f2$ are used to return floating point values from function/procedures.

### 8.7 Registers Preservation Conventions

The MIPS calling convention requires that only specific registers (not all) be saved across procedure calls.

- Integer registers $s0$ - $s7$ must be saved by the procedure.
- Floating point registers $f20$ - $f30$ must be saved the procedure.

When writing a procedure, this will require that the registers $s0$ - $s7$ or $f20$ - $f30$ (single or double precision) be pushed and popped from the stack if those registers are utilized/changed. When calling a procedure, the main routine must be written so that any values required across procedure calls be placed in register $s0$ - $s7$ or $f20$ - $f30$ (single or double precision).

Integer registers $t0$ - $t9$ and floating point registers $f4$ - $f10$ and $f16$ - $f18$ (single or double precision) are used to hold temporary quantities that do not need to be preserved across procedure calls.
8.8 Miscellaneous Register Usage

Registers $at, $k0, and $k1 are reserved for the assembler and operating system and should not be used by programs. Register $fp is used to point to the procedure call frame on the stack. This can be used when arguments are passed on the stack.

Register $gp is used as a global point (to point to globally accessible data areas). This register is not typically used when writing assembly programs directly.

8.9 Summary, Callee Conventions

The calling convention addresses specific requirements for the *callee* or routine that is being called from another procedure (which includes the main routine).

- Push any altered "saved" registers on the stack.
  - Specifically, this includes $s0 - $s7, $f20 - $f30, $ra, $fp, or $gp.
  - If the procedure is a non-leaf procedure, $ra must be saved.
  - If $fp is altered, $fp must be saved which is required when arguments are passed on the stack
  - Space for local variables should be created on the stack for stack dynamic local variables.
- *Note*, when altering the $sp register, it should be done in a single operation (instead of a series).
- If arguments are passed on the stack, $fp should be set as follows
  - $fp = $sp + (frame size)
  - This will set $fp pointing to the first argument passed on the stack.

The procedure can access first 4 integer arguments in registers $a0 - $a3 and the first two float registers $f12 - $f14.

Arguments passed on the stack can be accessed using $fp. The procedure should place returned values (if any) into $v0 and $v1.

- Restore saved registers
  - Includes $s0 - $s7, $fp, $ra, $gp if they were pushed.
  - Return to the calling procedure via the jr $ra instruction.

The procedures example section provides a series of example procedures and functions including register usage and argument transmission.
## 8.10 Call Frame

The procedure/function call frame or activation record is what the information placed on the stack is called. As noted in the previous sections, the procedure call frame includes passed parameters (if any) and the preserved registers. In addition, space for the procedures local variables (if any) is allocated on the stack.

A general overview of the call frame is show as follows:

<table>
<thead>
<tr>
<th>Call Frame</th>
<th>Arguments</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>Preserved Registers</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>Local Variables</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Each part of the call frame may be a different size based on how many arguments are passed (if any), which registers must be preserved (if any), or the amount and size of the local variables (if any).

### 8.10.1.1 Stack Dynamic Local Variables

The local variables, also referred to as stack dynamic local variables, are typically allocated by the compiler and assigned to stack locations. This allows a more efficient use of memory for high-level languages. This can be very important in large programs.

For example, assume there are 10 procedures each with a locally declared 100,000 element array of integers. Since each integer typically requires 4-bytes, this would mean 400,000 bytes for each procedure with a combined total of 4,000,000 bytes (or about ~4MB) for all ten procedures.

For the standard method of stack dynamic local variables, each array is only allocated when the procedure is active (i.e., being executed). If none of the procedures is called, none of the memory is allocated. If only two of the arrays are active at any given time, only 800,000 bytes are allocated at any given time.
However, if the arrays were to be declared statically (i.e., not the standard local declaration), the ~4MB of memory allocated even if none of the procedures is ever called. This can lead to excessive memory usage which can slow a program down.

8.11 Procedure Examples
This section presents a series of example procedures of varying complexity.

8.11.1 Example Program, Power Function
This following is a very simple example function call. The example includes a simple main procedure and a simple function that computes $x^y$ (i.e., x to the y power). The high-level language call, shown in C/C++ here, would be:

```c
answer = power(x, y);
```

Where $x$ and $y$ are passed by value and the result return to the variable $answer$. The main passes the arguments by value and receives the result in $v0$ (as per the convention). The main then saves the result into the variable $answer$.

```asm
# Example function to demonstrate calling conventions
# Function computes power (i.e., x to y power).

# -----------------------------------------------
# Data Declarations
.data
  x: .word 3
  y: .word 5
  answer: .word 0

# -----------------------------------------------
# Main routine.
# Call simple procedure two add two numbers.
.text
.globl main
.ent main
main:
```
lw $a0, x  # pass arg's to function
lw $a1, y
jal power
sw $v0, answer

li $v0, 10
syscall  # terminate program
.end main

# --------------------------------------------------
# Function to find and return x^y

# -----  
# Arguments  
# $a0 = x  
# $a1 = y  
# Returns  
# $v0 = x^y

.globl power
.ent power
power:
    li $v0, 1
    li $t0, 0

powLoop:
    mul $v0, $v0, $a0
    add $t0, $t0, 1
    blt $t0, $a1, powLoop

jr $ra
.end power

Refer to the next section for a more complex example.

8.11.2  Example program, Summation Function
This following is an example procedure call.
# Example function to demonstrate calling conventions.
# Simple function to sum six arguments.

# ---------------------------------
# Data Declarations

.data

num1:   .word 3
num2:   .word 5
num3:   .word 3
num4:   .word 5
num5:   .word 3
num6:   .word 5
sum:    .word 0

# ---------------------------------
# Main routine.
# Call function to add six numbers.
# First 4 arguments are passed in $a0-$a3.
# Next 2 arguments are passed on the stack.

.text

.globl main
.ent main
main:
    lw      $a0, num1         # pass arg's and procedure
    lw      $a1, num2
    lw      $a2, num3
    lw      $a3, num4
    lw      $t0, num5
    lw      $t1, num6
    subu    $sp, $sp, 8
    sw      $t0, ($sp)
    sw      $t1, 4($sp)
    jal     addem
    sw      $v0, sum
addu $sp, $sp, 8          # clear stack

li $v0,10
syscall                 # terminate
.end main

# ------------------------------
# Example function to add 6 numbers

# -----  
# Arguments
# $a0 - num1
# $a1 - num2
# $a2 - num3
# $a3 - num4
# ($fp) - num5
# 4($fp) - num6

# Returns
# $v0 - num1+num2+num3+num4+num5+num6

.globl addem
.ent addem
addem:

subu $sp, $sp, 4          # preserve registers
sw $fp, ($sp)

addu $fp, $sp, 4          # set frame pointer

# -----  
# Perform additions.

li $v0, 0
add $v0, $v0 $a0           # num1
add $v0, $v0 $a1           # num2
add $v0, $v0 $a2           # num3
add $v0, $v0 $a3           # num4
lw $t0, ($fp)              # num5
add $v0, $v0 $t0
Chapter 8.0 Procedures/Functions

lw $t0, 4($fp) # num6
add $v0, $v0, $t0

# -----
# Restore registers.

lw $fp, ($sp)
addu $sp, $sp, 4
jr $ra
.end addem

Refer to the next section for a more complex example.

8.11.3 Example Program, Pythagorean Theorem Procedure

The following is an example of a procedure that calls another function. Given the $a$ and $b$ sides of a right triangle, the $c$ side can be computed as follows:

\[ c_{\text{Side}} = \sqrt{a_{\text{Side}}^2 + b_{\text{Side}}^2} \]

This example program will call a procedure to compute the $c$ sides of a series of right triangles. The $a$ sides and $b$ sides are stored in an arrays, $aSides[]$ and $bSides[]$ and results stored into an array, $cSides[]$. The procedure will also compute the minimum, maximum, sum, and average of the $cSides[]$ values. All values are integers. In order to compute the integer square root, a $iSqrt()$ function is used. The $iSqrt()$ function uses a simplified version of Newtons method.

# Example program to calculate the cSide for each right triangle in a series of right triangles given the aSides and bSides using the Pythagorean theorem.

# Pythagorean theorem:
# $c_{\text{Side}} = \sqrt{a_{\text{Side}}^2 + b_{\text{Side}}^2}$

# Provides examples of MIPS procedure calling.
# -----------------------------------------------
# Data Declarations

.data

aSides: .word 19, 17, 15, 13, 11, 19, 17, 15, 13, 11
  .word 12, 14, 16, 18, 10
bSides: .word 34, 32, 31, 35, 34, 33, 32, 37, 38, 39
  .word 32, 30, 36, 38, 30
cSides: .space 60

length: .word 15

min: .word 0
max: .word 0
sum: .word 0
ave: .word 0

# text/code section

.text
.globl main
.ent main
main:

# -----
# Main program calls the cSidesStats routine.
# The HLL call is as follows:
#   cSidesStats(aSides, bSides, cSides, length, min,
#                 max, sum, ave)
# Note:
#   The arrays are passed by reference
#   The length is passed by value
#   The min, max, sum, and ave are passed by reference.

la $a0, aSides  # address of array
la $a1, bSides  # address of array
la $a2, cSides  # address of array
lw $a3, length     # value of length
la $t0, min       # address for min
la $t1, max       # address for max
la $t2, sum       # address for sum
la $t3, ave       # address for ave

subu $sp, $sp, 16
sw $t0, ($sp)    # push addresses
sw $t1, 4($sp)
sw $t2, 8($sp)
sw $t3, 12($sp)

jal cSidesStats  # call routine

addu $sp, $sp, 16 # clear arguments

# ----- 
# Done, terminate program.

li $v0, 10        # code for terminate
syscall           # system call

.end main

# --------------------------------- 
# Procedure to calculate the cSides[] for each right
# triangle in a series of right triangles given the
# aSides[] and bSides[] using the Pythagorean theorem.

# Pythagorean theorem formula:
# cSides[n] = sqrt ( aSides[n]^2 + bSides[n]^2 )

# Also finds and returns the minimum, maximum, sum, and average for the cSides.

# Uses the iSqrt() routine to find the integer square root of an integer.

# ----- 
# Arguments:
# $a0 - address of aSides[
# $a1 - address of bSides[
# $a2 - address of cSides[
# $a3 - list length
# ($fp) - addr of min
# 4($fp) - addr of max
# 8($fp) - addr of sum
# 12($fp) - addr of ave

# Returns (via passed addresses):
#  cSides[
#  min
#  max
#  sum
#  ave

.globl  cSidesStats
.ent   cSidesStats

cSidesStats:
    subu  $sp, $sp, 20          # preserve registers
    sw    $s0, 0($sp)
    sw    $s1, 4($sp)
    sw    $s2, 8($sp)
    sw    $s3, 12($sp)
    sw    $fp, 16($sp)
    sw    $ra, 20($sp)

    addu  $fp, $sp, 20          # set frame pointer

# -----  
# Loop to calculate cSides[]
# Note, must use $s<n> registers due to iSqrt() call

move  $s0, $a0               # address of aSides
move  $s1, $a1               # address of bSides
move  $s2, $a2               # address of cSides
li    $s3, 0                 # index = 0


cSidesLoop:
    lw    $t0, ($s0)           # get aSides[n]
mul $t0, $t0, $t0  # aSides[n]^2
lw  $t1, ($s1)   # get bSides[n]
mul $t1, $t1, $t1  # bSides[n]^2
add $a0, $t0, $t1
jal iSqrt  # call iSqrt()

sw  $v0, ($s2)  # save to cSides[n]

addu $s0, $s0, 4  # update aSides addr
addu $s1, $s1, 4  # update bSides addr
addu $s2, $s2, 4  # update cSides addr
addu $s3, $s3, 1  # index++

blt $s3, $a3, cSidesLoop  # if index<len, loop

# -----  
# Loop to find minimum, maximum, and sum.

move $s2, $a2  # strt addr of cSides
li  $t0, 0  # index = 0
lw  $t1, ($s2)  # min = cSides[0]
lw  $t2, ($s2)  # max = cSides[0]
li  $t3, 0  # sum = 0

statsLoop:
  lw  $t4, ($s2)  # get cSides[n]
  bge $t4, $t1, notNewMin  # if cSides[n] >= # item -> skip
  move $t1, $t4  # set new min value
notNewMin:
  ble $t4, $t2, notNewMax  # if cSides[n] <= # item -> skip
  move $t2, $t4  # set new max value
notNewMax:
  add $t3, $t3, $t4  # sum += cSides[n]
  addu $s2, $s2, 4  # update cSides addr
```
addu $t0, $t0, 1       # index++

blt $t0, $a3, statsLoop  # if indx<len -> loop

lw  $t5, ($fp)           # get address of min
sw  $t1, ($t5)           # save min
lw  $t5, 4($fp)          # get address of max
sw  $t2, ($t5)           # save max
lw  $t5, 8($fp)          # get address of sum
sw  $t3, ($t5)           # save sum

div $t0, $t3, $a3        # ave = sum / len
lw  $t5, 12($fp)         # get address of ave
sw  $t0, ($t5)           # save ave

# ------
# Done, restore registers and return to calling routine.

lw  $s0, 0($sp)
lw  $s1, 4($sp)
lw  $s2, 8($sp)
lw  $s3, 12($sp)
lw  $fp, 16($sp)
lw  $ra, 20($sp)
addu $sp, $sp, 20
jr  $ra

.end  cSidesStats

# --------------------
# Function to computer integer square root for
# an integer value.

# Uses a simplified version of Newtons method.
# x = N
# iterate 20 times:
#     x' = (x + N/x) / 2
#     x = x'
```
# -----
# Arguments
#   $a0 - N
#
# Returns
#   $v0 - integer square root of N

.globl iSqrt
.ent iSqrt

iSqrt:
  move $v0, $a0           # $t0 = x = N
  li $t0, 0                # counter

sqrLoop:
  div $t1, $a0, $v0        # N/x
  add $v0, $t1, $v0        # x + N/x
  div $v0, $v0, 2          # (x + N/x)/2

  add $t0, $t0, 1
  blt $t0, 20, sqrLoop

  jr $ra
.end iSqrt

This example uses a simplified version of Newton's method. Further improvements are left to the reader as an exercise.
9.0 QtSpim System Service Calls

The operating system must provide some basic services for functions that a user program can not easily perform on its own. Some key examples include input and output operations. These functions are typically referred to as system services. The QtSpim simulator provides a series of operating system like services by using a syscall instruction.

To request a specific service from the QtSpim simulator, the 'call code' is loaded in the $v0 register. Based on the specific system service being requested, additional information may be needed which is loaded in the argument registers (as noted in the Procedures/Functions section).

9.1 Supported QtSpim System Services

A list of the supported system services are listed in the below table. A series of examples is provided in the following sections.

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Call Code</th>
<th>Input</th>
<th>Output</th>
</tr>
</thead>
<tbody>
<tr>
<td>Print Integer (32-bit)</td>
<td>1</td>
<td>$a0 → integer to be printed</td>
<td></td>
</tr>
<tr>
<td>Print Float (32-bit)</td>
<td>2</td>
<td>$f12 → 32-bit floating-point value to be printed</td>
<td></td>
</tr>
<tr>
<td>Print Double (64-bit)</td>
<td>3</td>
<td>$f12 → 64-bit floating-point value to be printed</td>
<td></td>
</tr>
<tr>
<td>Print String</td>
<td>4</td>
<td>$a0 → starting address of NULL terminated string to be printed</td>
<td></td>
</tr>
<tr>
<td>Read Integer (32-bit)</td>
<td>5</td>
<td></td>
<td>$v0 → 32-bit integer entered by user</td>
</tr>
<tr>
<td>Read Float (32-bit)</td>
<td>6</td>
<td></td>
<td>$f0 → 32-bit floating-point value entered by user</td>
</tr>
<tr>
<td>Read Double (64-bit)</td>
<td>7</td>
<td></td>
<td>$f0 → 64-bit floating-point value entered by user</td>
</tr>
</tbody>
</table>
### Chapter 9.0 QtSpim System Service Calls

The file open access flags are defined as follows:

- **Read** = 0x0, **Write** = 0x1, **Read/Write** = 0x2
- **OR Create** = 0x100, **Truncate** = 0x200, **Append** = 0x8
- **OR Text** = 0x4000, **Binary** = 0x8000

For example, for a file read operation the 0x0 would be selected. For a file write operation, the 0x1 would be selected.

<table>
<thead>
<tr>
<th>Method</th>
<th>Service</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Read String</td>
<td>8</td>
<td>$a0 → starting address of buffer (of where to store character entered by user) $a1 → length of buffer</td>
</tr>
<tr>
<td>Allocate Memory</td>
<td>9</td>
<td>$a0 → number of bytes to allocate $v0 → starting address of allocated memory</td>
</tr>
<tr>
<td>Terminate</td>
<td>10</td>
<td></td>
</tr>
<tr>
<td>Print Character</td>
<td>11</td>
<td>$a0 → character to be printed $v0 → character entered by user</td>
</tr>
<tr>
<td>Read Character</td>
<td>12</td>
<td>$a0 → file name string, NULL terminated $a1 → access flags $a2 → file mode, (UNIX style) $v0 → file descriptor</td>
</tr>
<tr>
<td>File Open</td>
<td>13</td>
<td>$a0 → file name string, NULL terminated $a1 → access flags $a2 → file mode, (UNIX style) $v0 → file descriptor</td>
</tr>
<tr>
<td>File Read</td>
<td>14</td>
<td>$a0 → file descriptor $a1 → buffer starting address $a2 → number of bytes to read $v0 → number of bytes actually read from file (-1 = error, 0 = end of file)</td>
</tr>
<tr>
<td>File Write</td>
<td>15</td>
<td>$a0 → file descriptor $a1 → buffer starting address $a2 → number of bytes to read $v0 → number of bytes actually written to file (-1 = error, 0 = end of file)</td>
</tr>
<tr>
<td>File Close</td>
<td>16</td>
<td>$a0 → file descriptor</td>
</tr>
</tbody>
</table>
9.2 QtSpim System Services Examples

This section provides a series of examples using system service calls.

The system service calls follow the standard calling convention in that the temporary registers ($t0 - t9$) may be altered and the saved registers ($s0 - s7, fp, ra$) will be preserved. As such, if a series of values is being printed in a loop, it saved register would be required for the loop counter and the current array address/index.

9.2.1 Example Program, Display String and Integer

The following code provides an example of how to display a string and an integer.

```assembly
# Example program to display a string and an integer.
# Demonstrates use of QtSpim system service calls.

# Data Declarations
.data
hdr:     .ascii     "Example\n"
        .asciiz    "The meaning of life is: "
number:  .word      42

# text/code section
.text
.globl   main
main:
    la    $a0, hdr       # addr of NULL terminated string
    li    $v0, 4         # call code for print string
    syscall          # system call

    li    $v0, 1         # call code for print integer
    lw    $a0, number    # value for integer to print
    syscall          # system call

# -----
# Done, terminate program.
```
li $v0, 10 # call code for terminate
syscall # system call
.end main

Note, in this example, the string definition ensures the NULL termination as required by the system service.

The output for the example would be displayed to the QtSpim console window. For example,

![Console window](image)

The console window can be display or hidden from the Windows menu (on the top bar).

### 9.2.2 Example Program, Read Integer

The following code provides an example of how to display a prompt string, read an integer value, square that integer value, and display the final result.

```assembly
# Example program to display an array.
# Demonstrates use of QtSpim system service calls.

# -------------------------------
# Data Declarations

.data
hdr:    .ascii   "Squaring Example
ansMsg: .asciiz  "Enter Value: "
value:   .word    0

# text/code section
```
The output for the example would be displayed to the QtSpim console window. For example,
Note, the default console window size will typically be larger than what is shown above.

### 9.2.3 Example Program, Display Array

The following code provides an example of how to display an array. In this example, an array of numbers is displayed to the screen five number per line (arbitrarily chosen) to make the output appear more pleasing.

Since the system service call is utilized for the print function, the saved register must be used. Refer to the Procedures/Functions section for additional information regarding the MIPS calling conventions.

```plaintext
# Example program to display an array.
# Demonstrates use of QtSpim system service calls.

# Data Declarations

.data
hdr:    .ascii  "Array Values\n"
        .asciiz  "--------------------------\n\n"
spaces: .asciiz  "   
newLine: .asciiz  "\n"
array:   .word  11, 13, 15, 17, 19
         .word  21, 23, 25, 27, 29
         .word  31, 33, 35, 37, 39
         .word  41, 43, 45, 47
length:  .word  19

# text/code section

.text
.globl main
main:
    li  $v0, 4       # print header string
    la  $a0, hdr
    syscall
```
la  $s0, array
li  $s1, 0
lw  $s2, length

printLoop:
  li  $v0, 1                     # call code for print
  Integer
  lw  $a0, ($s0)               # get array[i]
  syscall                      # system call
  li  $v0, 4                   # print spaces
  la  $a0, spaces
  syscall

  addu $s0, $s0, 4             # update addr (next word)
  add  $s1, $s1, 1             # increment counter

  rem  $t0, $s1, 5
  bnez  $t0, skipNewLine

  li  $v0, 4                   # print new line
  la  $a0, newLine
  syscall

skipNewLine:
  bne  $s1, $s2, printLoop     # if cnter<len -> loop

# -----  
# Done, terminate program.

  li  $v0, 10                  # call code for terminate
  syscall                      # system call
.end main

The output for the example would be displayed to the QtSpim console window.
For example,

The example codes does not align the values (when printed). The values above appear aligned only since they are all the same size.
10.0 Multi-dimension Array Implementation

This chapter provides a summary of the implementation of multiple dimension array as viewed from assembly language.

Memory is inherently a single dimension entity. As such, multi-dimension array is implemented as sets of single dimension array. There are two primary ways this can be performed; row major and column major. Each is explained in subsequent sections.

To simplify the explanation, this section focuses on two-dimensional arrays. The general process extents to high dimensions.

10.1 High-Level Language View

Multi-Dimension arrays are sometimes used in high level languages. For example, in C/C++, the declaration of: \texttt{int arr[3][4]} would declare an array as follows:

\begin{verbatim}
arr[1][0]  arr[1][1]  arr[1][2]  arr[1][3]
arr[0][0]  arr[0][1]  arr[0][2]  arr[0][3]
\end{verbatim}

It is expected that the reader is generally familiar with the high-level language use of two-dimensional arrays.
10.2 Row-Major

Row-major assigns each row as a single dimension array in memory, one row after the next until all rows are in memory.

![Array Diagram]

The formula to convert two-dimensional array indexes (row, column) into a single dimension, row-major memory offset is as follows:

\[
addr = baseAddr + (rowIndex * rowSize + colIndex) * dataSize
\]

Where the base address is the starting address of the array, dataSize is the size of the data in bytes, and rowSize is the dimension or number of the rows in the array. In this example, the number of columns in the array is 4 (from the previous high-level language declaration).

For example, to access the \texttt{arr[1][2]} element (labeled '6' in the above diagram), assuming the array is composed of 32-bit sized elements it would be:

\[
\text{address} = \text{arr} + (1 * 4 + 2) * 4 = \text{arr} + (4 + 2) * 4 \\
= \text{arr} + 6 * 4 = \text{arr} + 24
\]

Which generates the correct, final address.
10.3 Column-Major

Column-major assigns each column as a single dimension array in memory, one column after the next until all rows are in memory.

The formula to convert two-dimensional array indexes (row, column) into a single dimension, column-major memory offset is as follows:

\[
addr = baseAddr + (colIndex \times colSize + rowIndex) \times dataSize
\]

Where the base address is the starting address of the array, dataSize is the size of the data in bytes, and colSize is the dimension or number of the columns in the array. In this example, the number of rows in the array is 3 (from the previous high-level language declaration).

For example, to access the \( \text{arr}[1][2] \) element (labeled '6' in the above diagram), assuming the array is composed of 32-bit sized elements it would be:

\[
address = arr + (2 \times 3 + 1) \times 4 = arr + (6 + 1) \times 4 = arr + 7 \times 4 = arr + 28
\]

Which generates the correct, final address.
10.4 Example Program, Matrix Diagonal Summation

The following code provides an example of how to access elements in a two-dimensional array. This example adds the elements on the diagonal of a two-dimensional array.

For example, given the logical view of a five-by-five square matrix:

<p>| | | | | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>11</td>
<td>12</td>
<td>13</td>
<td>14</td>
<td>15</td>
</tr>
<tr>
<td>16</td>
<td>17</td>
<td>18</td>
<td>19</td>
<td>20</td>
</tr>
<tr>
<td>21</td>
<td>22</td>
<td>23</td>
<td>24</td>
<td>25</td>
</tr>
<tr>
<td>26</td>
<td>27</td>
<td>28</td>
<td>29</td>
<td>30</td>
</tr>
<tr>
<td>31</td>
<td>32</td>
<td>33</td>
<td>34</td>
<td>35</td>
</tr>
</tbody>
</table>

The main diagonal contains the numbers, 11, 17, 23, 29, and 35.

```
# Example program to compute the sum of diagonal in a square two-dimensional array
# Demonstrates multi-dimension array indexing.
# Assumes row-major ordering.

# Data Declarations

.data

mdArray: .word 11, 12, 13, 14, 15
         .word 16, 17, 18, 19, 20
         .word 21, 22, 23, 24, 25
         .word 26, 27, 28, 29, 30
         .word 31, 32, 33, 34, 35

size:   .word 5
dSum:   .word 0

DATASIZE = 4  # 4 bytes for words
```
```assembly
finalMsg: .ascii "Two-Dimensional Diagonal"
        .ascii "Summation\n\n"
        .asciiz "Diagonal Sum = "

# -----------------------------------------------
# Text/code section

.text
.globl    main
main:

# -----
# Call function to sum the diagonal
#    (of square two-dimensional array)

    la    $a0, mdArray    # base address of array
    lw    $a1, size      # array size

    jal   diagSummer

    sw    $v0, dSum

# -----
# Display final result.

    li    $v0, 4         # print prompt string
    la    $a0, finalMsg
    syscall

    li    $v0, 1         # print integer
    lw    $a0, dSum
    syscall

# -----
# Done, terminate program.

    li    $v0, 10        # call code for terminate
    syscall              # system call
.end main
```
Chapter 10.0  ▶  Multi-dimension Array Implementation

# Simple function to sum the diagonals of a square two-dimensional array.

# Approach
# loop i = 0 to len-1
#   sum = sum + mdArray[i][i]

# Note, for two-dimensional array:
# addr = baseAddr+(rowIndex*colSize+colIndex) * dataSize
# Since the two-dimensional array is given as square, the row and column dimensions are the same (i.e., size).

# ----
# Arguments
# $a0 - array base address
# $a1 - size (of square two-dimension array)

# Returns
# $v0 - sum of diagonals

.globl diagSummer
.ent diagSummer

diagSummer:

    li $v0, 0      # sum=0
    li $t1, 0      # loop index, i=0

diagSumLoop:
    mul $t3, $t1, $a1  # (rowIndex * colSize
    add $t3, $t3, $t1  # + colIndex)
    # note, rowIndex=colIndex
    mul $t3, $t3, DATASIZE  # * dataSize
    add $t4, $a0, $t3      # + base address
    lw $t5, ($t4)        # get mdArray[i][i]
    add $v0, $v0, $t5    # sum = sum+mdArray[i][i]
add $t1, $t1, 1       # i = i + 1
blt $t1, $a1, diagSumLoop

# -----
# Done, return to calling routine.

jr $ra
.end diagSummer

While not mathematically useful, this does demonstrate how elements in a two-dimensional array.
11.0 Recursion

The Google search result for recursion, shows *Recursion, did you mean recursion?*

Recursion is the idea that a function may call itself (which is the basis for the joke). Recursion is a powerful general-purpose programming technique and is used for some important applications including searching and sorting.

Recursion can be very confusing in its simplicity. The simple examples in this section will not be enough in themselves for the reader to obtain recursive enlightenment. The goal of this section is to provide some insight into the underlying mechanisms that support recursion. The simple examples here which are used introduce recursion are meant to help demonstrate the form and structure for recursion. More complex examples (than will be discussed here) should be studied and implemented in order to ensure a complete appreciation for the power of recursion.

The procedure/function calling process previously described supports recursion without any changes.

A recursive function must have a recursive definition that includes:

1. base case, or cases, that provide a simple result (that defines when the recursion should stop).
2. rule, or set of rules, that reduce toward the base case.

This definition is referred to as a recursive relation.

11.1 Recursion Example, Factorial

The factorial function is mathematically defined as follows:

\[ n! = \prod_{k=1}^{n} k \]

Or more familiarly, you might see 5! as:

\[ 5! = 5 \times 4 \times 3 \times 2 \times 1 \]

It must be noted that this function could easily be computed with a loop. However, the reason this is done recursively is to provide a simple example of how recursion works.
A typical recursive definition for factorial is:

\[ \text{factorial}(n) = \begin{cases} 
1 & \text{if } n = 0 \\
 n \times \text{factorial}(n-1) & \text{if } n \geq 1 
\end{cases} \]

This definition assumes that the value of \( n \) is positive.

### 11.1.1 Example Program, Recursive Factorial Function

The following code provides an example of the recursive factorial function.

```
# Example program to demonstrate recursion.
# ---------------------------------------------------------------
# Data Declarations

.data

prompt: .ascii "Factorial Example Program\n\n" 
        .asciiz "Enter N value: " 
results: .asciiz "\nFactorial of N = "

n:      .word 0
answer:  .word 0

# ---------------------------------------------------------------
# Text/code section

.text
.globl main
main:

# -----
# Read n value from user

    li $v0, 4             # print prompt string
    la $a0, prompt
    syscall

    li $v0, 5             # read N (as integer)
```
Chapter 11.0 ► Recursion

```
syscall
sw  $v0, n

# -----
# Call factorial function.

lw  $a0, n
jal  fact
sw  $v0, answer

# -----
# Display result

li  $v0, 4          # print prompt string
la  $a0, results
syscall

li  $v0, 1          # print integer
lw  $a0, answer
syscall

# -----
# Done, terminate program.

li  $v0, 10         # call code for terminate
syscall             # system call
.end main

# ------------------------------------------------------------------
# Factorial function
# Recursive definition:
#    = 1     if n = 0
#    = n * fact(n-1) if n >= 1

# -----
# Arguments
#    $a0 - n
# Returns
#    $v0 set to n!
```
Chapter 11.0 ▪ Recursion

```
.globl  fact
.ent  fact

fact:
    subu  $sp, $sp, 8
    sw    $ra, ($sp)
    sw    $s0, 4($sp)

    li      $v0, 1              # check base case
    beq     $a0, 0, factDone

    move    $s0, $a0            # fact(n-1)
    sub      $a0, $a0, 1
    jal     fact

    mul     $v0, $s0, $v0       # n * fact(n-1)

factDone:
    lw       $ra, ($sp)
    lw       $s0, 4($sp)
    addu    $sp, $sp, 8
    jr       $ra

.end  fact
```

The output for the example would be displayed to the QtSpim console window. For example;

Refer to the next section for an explanation of how this function works.
11.1.2 Recursive Factorial Function Call Tree

In order to help understand recursion, a recursion tree can show how the recursive calls interact.

When the initial call occurs from main, the main will start into the `fact()` function (shown as step 1). Since the argument, of 5 is not a base case, the `fact()` function must call `fact()` again with the argument of \( n-1 \) or 4 in this example (step 2). And, again, since 4 is not the base case, the `fact()` function must call `fact()` again with the argument of \( n-1 \) or 3 in this example (step 3).

This process continues until the argument passed into the `fact()` function meets the base case which is when the arguments is equal to 1 (shown as step 5). When this occurs, only then is a return value provided to the previous call (step 6). This return argument is
then used to calculate the previous multiplication which is 2 times 1 which will return a value to the previous call (as shown in step 7).

This returns will continue (steps 8, 9, and 10) until the main has a final answer.

Since the code being executed is the same, each instance of the `fact()` function is different from any other instance only in the arguments and temporary values. The arguments and temporary values for each instance are different since they maintained on the stack as required by the standard calling convention.

For example, consider a call to factorial with \( n = 2 \) (step 4 on the diagram). The return address, $ra$, and previous contents of $s0$ are preserved by pushing them on the stack in accordance with the standard calling convention. The base case is checked and since \( n \neq 1 \) it continues to save the original value of 1 into $s0$, decrement the original argument, \( n \), by 1 and calling the `fact()` function (with \( n = 1 \)). The call the `fact()` function (step 5 in the diagram) is like any other function call in that it must follow the standard calling convention, which requires preserving $ra$ and $s0$. This when the function returns an answer, 1 in this specific case, that answer in $v0$ is multiplied by the original \( n \) value in $s0$ and returned to the calling routine.

As such the foundation for recursion is the procedure call frame or activation record. In general, it is simply stated that recursion is stack-based.

It should also be noted that the height of the recursion tree is directly associated with the amount of stack memory used by the function.

### 11.2 Recursion Example, Fibonacci

The Fibonacci function is mathematically defined as follows:

\[
F_n = F_{n-1} + F_{n-2}
\]

for positive integers with seed values of \( F_0 = 0 \) and \( F_1 = 1 \) by definition.

As such, starting from 0 the first 14 numbers in the Fibonacci series are:

\[
0, 1, 1, 2, 3, 5, 8, 13, 21, 34, 55, 89, 144, 233
\]

It must be noted that this function could easily be computed with a loop. However, the reason this is done recursively is to provide a simple example of how recursion works.
For example, a typical recursive definition for Fibonacci is:

\[
fib(n) = \begin{cases} 
0 & \text{if } n=0 \\
1 & \text{if } n=1 \\
fib(n-1) + fib(n-2) & \text{if } n>1
\end{cases}
\]

This definition assumes that the value of \( n \) is positive.

### 11.2.1 Example Program, Recursive Fibonacci Function

The following code provides an example of the recursive Fibonacci function.

```assembly
# Recursive Fibonacci program to demonstrate recursion.
#
# Data Declarations
.data
prompt: .ascii "Fibonacci Example Program

" results: .asciiz "\nFibonacci of N = 

n: .word 0 answer: .word 0

# Text/code section
.text
.globl main
main:

# ----- # Read n value from user
    li $v0, 4 # print prompt string
```
Chapter 11.0 ▸ Recursion

```assembly
la $a0, prompt
syscall

li $v0, 5                 # read N (as integer)
syscall

sw $v0, n

# -----
# Call Fibonacci function.

lw $a0, n
jal fib

sw $v0, answer

# -----
# Display result

li $v0, 4                 # print prompt string
la $a0, results
syscall

li $v0, 1                 # print integer
lw $a0, answer
syscall

# -----
# Done, terminate program.

li $v0, 10                # call code for terminate
syscall                   # system call

.end main
```

# -------------------------------
# Fibonacci function

# Recursive definition:
# = 0                if n = 0
# = 1                if n = 1
# = fib(n-1) + fib(n-2) if n > 2
# ------
# Arguments
# $a0 - n

# Returns
# $v0 set to fib(n)

.globl fib
.ent fib
fib:
    subu $sp, $sp, 8
    sw $ra, ($sp)
    sw $s0, 4($sp)

    move $v0, $a0                  # check for base cases
    ble $a0, 1, fibDone

    move $s0, $a0                  # get fib(n-1)
    sub  $a0, $a0, 1
    jal  fib

    move $a0, $s0                  # set n-2
    sub  $a0, $a0, 2
    move $s0, $v0                  # save fib(n-1)
    jal  fib

    add  $v0, $s0, $v0              # fib(n-1)+fib(n-2)

fibDone:
    lw  $ra, ($sp)
    lw  $s0, 4($sp)
    addu $sp, $sp, 8
    jr   $ra
.end fib

The output for the example would be displayed to the QtSpim console window.
Chapter 11.0  Recursion

For example;

Refer to the next section for an explanation of how this function works.

11.2.2  Recursive Fibonacci Function Call Tree

The Fibonacci recursion tree appears more complex than the previous factorial tree since the Fibonacci functions uses two recursive calls. However, the general process and use of the stack for arguments and temporary values is the same.

As noted in the factorial example, the basis of recursion is the stack. In this example, since two recursive calls are made, the first call will make another call, which may make yet another call. In this manner, the call sequence will follow the order shown in the following diagram.
The following is an example of the call tree for a Fibonacci call with $n = 4$.

The calls are shown with a solid line and the returns are shown with a dashed line.
Chapter 11.0 Recursion
12.0 Appendix A – Example Program

Below is a simple example program. This program can be used to test the simulator installation and as an example of the required program formatting.

```assembly
# Example program to find the minimum and maximum from a list of numbers.

# --------------------------------------------------------------------------------
# data segment

.data

array: .word 13, 34, 16, 61, 28
        .word 24, 58, 11, 26, 41
        .word 19, 7, 38, 12, 13
len:   .word 15

hdr:   .ascii "Example program to find max and"
        .asciiz " min

newLine: .asciiz "\n"

a1Msg: .asciiz "min = "

a2Msg: .asciiz "max = "

# --------------------------------------------------------------------------------
# text/code segment
# QtSpim requires the main procedure to be named "main".

.text
.globl main
.ent main
main:

# This program will use pointers.
# t0 - array address
# t1 - count of elements
```
Appendix A – Example Program

# s2 – min
# s3 – max
# t4 – each word from array
# -----
# Display header
# Uses print string system call

    la  $a0, hdr
    li  $v0, 4
    syscall # print header

# -----
# Find max and min of the array.
# Set min and max to first item in list and then
# loop through the array and check min and max against
# each item in the list, updating the min and max values
# as needed.

    la  $t0, array       # set $t0 addr of array
    lw  $t1, len        # set $t1 to length
    lw  $s2, ($t0)      # set min, $t2 to array[0]
    lw  $s3, ($t0)      # set max, $t3 to array[0]

loop:
    lw  $t4, ($t0)       # get array[n]

    bge $t4, $s2, NotMin # is new min?
    move $s2, $t4       # set new min

NotMin:
    ble $t4, $s3, NotMax # is new max?
    move $s3, $t4       # set new max

    NotMax:
        sub $t1, $t1, 1   # decrement counter
        addu $t0, $t0, 4  # increment addr by word
        bnez $t1, loop

# -----
# Display results min and max.
# First display string, then value, then a print a
# new line (for formatting). Do for each max and min.

    la    $a0, a1Msg
    li    $v0, 4
    syscall          # print "min = "

    move  $a0, $s2
    li    $v0, 1
    syscall          # print min

    la    $a0, newLine   # print a newline
    li    $v0, 4
    syscall

    la    $a0, a2Msg
    li    $v0, 4
    syscall          # print "max = "

    move  $a0, $s3
    li    $v0, 1
    syscall          # print max

    la    $a0, newLine   # print a newline
    li    $v0, 4
    syscall

# -----
# Done, terminate program.
    li    $v0, 10
    syscall          # all done!

.end main
13.0 Appendix B – QtSpim Tutorial

This QtSpim Tutorial is designed to prepare you to use the QtSpim simulator and complete your MIPS assignments more easily.

13.1 Downloading and Installing QtSpim

The first step is to download and install QtSpim for your specific machine. QtSpim is available for Windows, Linux, and MAC OS’s.

13.1.1 QtSpim Download URLs

The following are the current URLs for QtSpim.

The QtSpim home page is located at:

http://spimsimulator.sourceforge.net/

The specific download site is located at:

http://sourceforge.net/projects/spimsimulator/files/

At the download site there are multiple versions for different target machines. These include Windows (all versions), Linux (32-bit), Linux (64-bit), and Mac OS (all versions). Download the latest version for your machine.

These URLS are subject to change. If they do not work, a Google search will find the correct URLS.

13.1.2 Installing QtSpim

Once the package is downloaded, follow the standard installation process for the specific OS being used. This typically will involve double-clicking the downloaded installation package and following the instructions. You will need administrator privileges to perform the installation. Additionally, some installations will require Internet access during the installation.
13.2 Working Directory

Create a working directory for the QtSpim assembly source files. This directory can be named anything, but must be legal on the chosen operating system.

13.3 Sample Program

Copy or type the provided example program (from Appendix A) to a file in your working directory. This file will be used in the remainder of the tutorial. It demonstrates assembler directives, procedure calls, console I/O, program termination, and good programming practice. Notice in particular the assembler directives '.data' and '.text' as well as the declarations of program constants. Understanding the basic flow of the example program will help you to complete your SPIM assignment quickly and painlessly. Once you have created the file and reviewed the code, it is time to move onto the next section.

13.4 QtSpim – Loading and Executing Programs

After the QtSpim application installation has been complete and the sample program has been created, you can execute the program to view the results. The use of QtSpim is described in the following sections.

13.4.1 Starting QtSpim

For Windows, this is typically, performed with the standard “Start Menu -> Programs -> QtSpim ” operation. For MAC OS, enter LaunchPad and click on QtSPim. For Linux, find the QtSpim icon (location is OS distribution dependent) and click on QtSpim.
13.4.2  Main Screen

The initial QtSpim screen will appear as shown below. There will be some minor difference based on the specific Operating System being used.

![Main Screen](image)

13.4.3  Load Program

To load the example program (and all programs), you can select the standard “File → Reinitialize and Load File” option from the menu bar. However, it is typically easier to select the Reinitialize and Load File Icon from the main screen (second file icon on right side).
Appendix B – QtSpim Tutorial

*Note*, the Load File option can be used on the initial load, but subsequent file loads will need to use the Reinitialize and Load File to ensure the appropriate reinitialization occurs.

**Reinitialize and Load File Icon**

![Reinitialize and Load File Icon](image)

Once selected, a standard open file dialog box will be displayed. Find and select 'asst0.asm' file (or whatever you named it) created in section 3.0.

![Open File Dialog](image)

Navigate as appropriate to find the example file previously created. When found, select the file (it will be highlighted) and click Open button (lower right hand corner).
The assembly process occurs as the file is being loaded. As such, any assembly syntax errors (i.e., misspelled instructions, undefined variables, etc.) are caught at this point. An appropriate error message is provided with a reference to the line number that caused the error.

When the file load is completed with no errors, the program is ready to run, but has not yet been executed. The screen will appear something like the following image.

![Image of QtSpim](image-url)

The code is placed in Text Window. The first column of hex values (in the []'s) is the address of that line of code. The next hex value is the OpCode or hex value of the 1's and 0's that the CPU understands to be that instruction.
Appendix B – QtSpim Tutorial

MIPS includes pseudo-instructions. That is an instruction that the CPU does not execute, but the programmer is allowed to use. The assembler, QtSpim here, accepts the instruction and inserts the real or **bare** instruction as appropriate.

### 13.4.4 Data Window

The data segment contains the data declared by your program (if any). To view the data segment, click on the Data Icon. The data window will appear similar to the following:

As before, the addresses are shown on the left side (with the []'s). The values at that address are shown in hex (middle) and in ASCII (right side). Depending on the specific type of data declarations, it may be easier to view the hex representation (i.e., like the...
array of numbers from the example code) or the ASCII representation (i.e., the declared strings).

*Note*, right clicking in the Data Window will display a menu allowing the user to change the default hex representation to decimal representation (if desired).

### 13.4.5 Program Execution

To execute the entire program (uninterrupted), you can select the standard “**Simulator → Run/Continue**” option from the menu bar. However, it is typically easier to select the **Run/Continue Icon** from the main screen or to type the **F5** key.

![Run/Continue Icon](image)

Once typed, the program will be execution.

If a program performs input and/or output, it will be directed to the Console window.
Appendix B – QtSpim Tutorial

For example, the sample program (from Appendix B) will display the following in the Console window when executed.

```
Example program to find max and min
min = 7
max = 61
```

For the sample program and the initial data set, these are the correct results.

### 13.4.6 Log File

QtSpim can create a log file documenting of the program results. To create a log file, you can select the standard “File → Save Log File” option from the menu bar. However, it is typically easier to select the Save Log File Icon from the main screen.
When selected, the Save Windows to Log File dialog box will be displayed as shown below on the left.

In general, the Text Segments and Console options should be selected as shown on the left. Based on the current version, selecting all will cause the simulator to crash.

Additionally, there is no default file name or location (for the log file). As such, a file name must be entered before it can be saved. This can done by manually entering the name in the Save to file box or by selecting the … box (on the lower right side).
Appendix B – QtSpim Tutorial

When the … option is selected, a Save to Log File dialog box is displayed allowing selection of a location and the entry of a file name.

When completed correctly, the Save Windows To Log File box will appear similar the below image.

When the options are selected and the file name entered, the OK box can be selected which will save the log file. This log file will need to be submitted as part the assignment submission.
### 13.4.7 Making Updates

In the highly unlikely event that the program does not work the first time or the program requirements are changed, the source file will need to be updated in a text editor. After the program source file is updated, it must be explicitly reloaded into QtSpim. The Reinitialize and Load File option must be used as described in section 4.3. Every change made to the source file must be re-loaded into QtSpim.

Once re-loaded, the program can be re-executed as noted in section 4.5. Refer to section 5.0 for information regarding debugging and controlled program execution.

### 13.5 Debugging

Often, looking at program source code will not help to find errors. The first step in debugging is to ensure that the file assembles correctly (or “reads” in the specific case of QtSpim). However, even if the file assembles, it still may not work correctly. In this case, the program must be debugged. In a broad sense, debugging is comparing the expected program results to actual program results. This requires a solid understanding of what the program is supposed to do and the specific order in which it does it → that is understanding the algorithm being used to solve the program. The algorithm should be noted in the program comments and can be used as a checklist for the debugging process.
Appendix B – QtSpim Tutorial

One potentially useful way to check the program status is to view the register contents. The current register contents are shown in the register window (left side) as shown in the image below.

The overall debugging process can be simplified by using the QtSpim controlled execution functions. These functions include single stepping through the program and using one or more breakpoints. A breakpoint is a programmer selected location in the program where execution will be paused. When the program is paused the current program status can be checked by viewing the register contents and/or the data segment. Typically, a breakpoint will be set, the program executed (to that point), and from there single stepping through the program watching execution and checking the results (via register contents and/or data segment).

When stepping through the program, the next instruction to be executed is highlighted. As such, that instruction has not yet been executed. This highlighting is how to track the progress of the program execution.

To set a breakpoint, select an appropriate location. This should be chosen with a specific expectation in mind. For example, if a program does not produce the correct average for a list of numbers, a typical debugging strategy would be to see if the sum is correct (as it is required for the average calculation). As such, a breakpoint could be set after the loop and before the average calculation.
As an example, to set a breakpoint after the loop in the sample program (from Appendix B), the first instruction after the loop can be found in the Text Window. This will require looking at the pseudo-instructions (on the right side of the Text Window).

The first instruction after the loop in the example program is highlighted in orange (for reference) in the image below.

*Note,* the orange highlighting was added in this document for reference and will not be displayed in QtSpim during normal execution.
Appendix B – QtSpim Tutorial

When an appropriate instruction is determined, move the cursor to the instruction address and right-click. The right-click will display the breakpoint menu as shown in the image below.

To set a breakpoint, select the Set Breakpoint option. If a breakpoint has already been set, it can be cleared by selecting the Clear Breakpoint option.
Once the breakpoint has been set, it will be highlighted with a small red icon such as an N as shown in the following image. *Note*, different operating systems may use a different icon.

Select the Run/Continue option (as described in section 4.5) which will execute the program up to the selected breakpoint.
Appendix B – QtSpim Tutorial

When program execution reaches the breakpoint, it will be paused and a Breakpoint dialog box display as shown in the below image.

![Breakpoint dialog box](image)

The program execution can be halted by selecting the Abort box. The breakpoint can be ignored, thus continuing to the next breakpoint or program termination, whichever comes first.
However, typically the Single Step box will be selected enter the single step mode. The following image shows the result of selecting Single Step. Note, the highlighted instruction represents the next instruction to be executed and thus has not yet been executed.
14.0 Appendix C – MIPS Instruction Set

This appendix presents a summary of the MIPS instructions as implemented within the QtSpim simulator. The instructions a grouped by like-operations and presented alphabetically.

The following table summarizes the notational conventions used.

<table>
<thead>
<tr>
<th>Operand Notation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rdest</td>
<td>Destination operand. Must be a register. Since it is a destination operand, the contents will be over written with the new result.</td>
</tr>
<tr>
<td>FRdest</td>
<td>Destination operand. Must be a floating-point register. Since it is a destination operand, the contents will be over written with the new result.</td>
</tr>
<tr>
<td>Rsfc</td>
<td>Source operand. Must be a register. Register value is unchanged.</td>
</tr>
<tr>
<td>FRscr</td>
<td>Source operand. Must be a floating-point register. Register value is unchanged.</td>
</tr>
<tr>
<td>Src</td>
<td>Source operand. Must be a register or an immediate value. Value is unchanged.</td>
</tr>
<tr>
<td>Imm</td>
<td>Immediate value</td>
</tr>
<tr>
<td>Mem</td>
<td>Memory location. May be a variable name or an indirect reference.</td>
</tr>
</tbody>
</table>

Refer to the chapter on Addressing Modes for more information regarding indirection.
Appendix C – MIPS Instruction Set

14.1 Arithmetic Instructions

Below are a summary of the basic integer arithmetic instructions.

- **abs**  \( R_{dest}, R_{src} \)  
  - **Absolute Value**
  - Sets \( R_{dest} = \text{absolute value of integer in } R_{src} \)

- **add**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Addition (with overflow)**
  - Sets \( R_{dest} = R_{src} + \text{Src} \) (or imm)

- **addu**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Addition (without overflow)**
  - Sets \( R_{dest} = R_{src} + \text{Src} \) (or imm)

- **div**  \( R_{src1}, R_{src} \)  
  - **Divide (with overflow)**
  - Set \( R_{lo} = R_{src1} / R_{src} \) (or imm)
  - Remainder is placed in \( R_{hi} \)

- **divu**  \( R_{src1}, R_{src} \)  
  - **Divide (without overflow)**
  - Set \( R_{lo} = R_{src1} / R_{src} \) (or imm)
  - Remainder is placed in \( R_{hi} \)

- **div**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Divide (with overflow)**
  - Sets: \( R_{dest} = R_{src} / \text{Src} \) (or imm)

- **divu**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Divide (without overflow)**
  - Sets: \( R_{dest} = R_{src} / \text{Src} \) (or imm)

- **mul**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Multiply (without overflow)**
  - Sets: \( R_{dest} = R_{src} \times \text{Src} \) (or imm)

- **mulo**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Multiply (with overflow)**
  - Sets: \( R_{dest} = R_{src} \times \text{Src} \) (or imm)

- **mulou**  \( R_{dest}, R_{src}, \text{Src} \)  
  - **Unsigned Multiply (with overflow)**
  - Sets: \( R_{lo} = R_{src} \times \text{Src} \) (or imm)
<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
</table>
| `mult Rsrc, Rsrc` | Multiply  
Sets $\text{hi:lo} = \text{Rscr} / \text{Src}$ (or imm) |
| `multu Rsrc, Rsrc` | Unsigned Multiply  
Sets $\text{hi:lo} = \text{Rscr} / \text{Src}$ (or imm) |
| `neg Rdest, Rsrc` | Negate Value (with overflow)  
Rdest = negative of integer in register Rsrc |
| `negu Rdest, Rsrc` | Negate Value (without overflow)  
Rdest = negative of integer in register Rsrc |
| `rem Rdest, Rsrc, Src` | Remainder after division  
Rdest = remainder from Rscr / Src (or imm) |
| `remu Rdest, Rsrc, Src` | Unsigned Remainder  
Rdest = remainder from Rscr / Src (or imm) |
| `sub Rdest, Rsrc, Src` | Subtract (with overflow)  
Rdest = Rsrc – Src (or imm) |
| `subu Rdest, Rsrc, Src` | Subtract (without overflow)  
Rdest = Rsrc – Src (or imm) |
## Appendix C – MIPS Instruction Set

### 14.2 Comparison Instructions

Below are a summary of the basic integer comparison instructions. Programmers generally use the conditional branch and jump instructions as detailed in the next section.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
</table>
| `seq Rdest, Rsrc1, Src2` | Set Equal  
  - Sets register Rdest to 1 if register Rsrc1 equals Src2 and to 0 otherwise |
| `sge Rdest, Rsrc1, Src2` | Set Greater Than Equal  
  - Sets register Rdest to 1 if register Rsrc1 greater than or equal Src2 and to 0 otherwise |
| `sgeu Rdest, Rsrc1, Src2` | Set Greater Than Equal Unsigned  
  - Sets register Rdest to 1 if register Rsnc1 is greater than or equal to Src2 and to 0 otherwise |
| `sgt Rdest, Rsnc1, Src2` | Set Greater Than  
  - Sets register Rdest to 1 if register Rsnc1 greater than Src2 and to 0 otherwise |
| `sgtu Rdest, Rsnc1, Src2` | Set Greater Than Unsigned  
  - Sets register Rdest to 1 if register Rsnc1 is greater than Src2 and to 0 otherwise |
| `sle Rdest, Rsnc1, Src2` | Set Less Than Equal  
  - Sets register Rdest to 1 if register Rsnc1 is less than or equal to Src2 and to 0 otherwise |
| `sleu Rdest, Rsnc1, Src2` | Set Less Than Equal Unsigned  
  - Sets register Rdest to 1 if register Rsnc1 is less than or equal to Src2 and to 0 otherwise |
### 14.3 Branch and Jump Instructions

Below are a summary of the basic conditional branch and jump instructions.

- **b label**
  - Branch instruction
  - Unconditionally branch to the instruction at the label

- **bczt label**
  - Branch Co-processor z True
  - Conditionally branch to the instruction at the label if co-processor z’s condition flag is true (false)
Appendix C – MIPS Instruction Set

**bczf** label

Branch Co-processor z False
- Conditionally branch to the instruction at the label if co-processor z’s condition flag is true (false)

**beq** Rsrcl, Src2, label

Branch on Equal
- Conditionally branch to the instruction at the label if the contents of register Rsrcl equals Src2

**beqz** Rsrcl, label

Branch on Equal Zero
- Conditionally branch to the instruction at the label if the contents of Rsrcl equals 0

**bge** Rsrcl, Src2, label

Branch on Greater Than Equal
- Conditionally branch to the instruction at the label if the contents of register Rsrcl are greater than or equal to Src2

**bgeu** Rsrcl, Src2, label

Branch on GTE Unsigned
- Conditionally branch to the instruction at the label if the contents of register Rsrcl are greater than or equal to Src2

**bgez** Rsrcl, label

Branch on Greater Than Equal Zero
- Conditionally branch to the instruction at the label if the contents of Rsrcl are greater than or equal to 0

**bgezal** Rsrcl, label

Branch on Greater Than Equal Zero and Link
- Conditionally branch to the instruction at the label if the contents of Rsrcl are greater than or equal to 0. Saves the address of the next instruction in $ra
<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
</table>
| `bgt Rscl, Src2, label` | Branch on Greater Than  
- Conditionally branch to the instruction at the label if the contents of register Rscl is greater than Src2 |
| `bgtu Rscl, Src2, label`  | Branch on Greater Than Unsigned  
- Conditionally branch to the instruction at the label if the contents of register Rscl are greater than Src2 |
| `bgtz Rs, label`         | Branch on Greater Than Zero  
- Conditionally branch to the instruction at the label if the contents of Rs are greater than 0 |
| `ble Rscl, Src2, label`  | Branch on Less Than Equal  
- Conditionally branch to the instruction at the label if the contents of register Rscl are less than or equal to Src2 |
| `bleu Rscl, Src2, label` | Branch on LTE Unsigned  
- Conditionally branch to the instruction at the label if the contents of register Rscl are less than or equal to Src2 |
| `blez Rs, label`         | Branch on Less Than Equal Zero  
- Conditionally branch to the instruction at the label if the contents of Rs are less than or equal to 0 |
| `bgezal Rs, label`      | Branch on Greater Than Equal Zero And  
Link  
- Conditionally branch to the instruction at the label if the contents of Rs are greater or equal to 0 or less than 0, respectively.  
Saves the address of the next instruction in register $ra |
bltzal  Rsrc, label
Branch on Less Than And Link
- Conditionally branch to the instruction at
the label if the contents of Rsrc are less
than 0 or less than 0, respectively. Save
the address of the next instruction in
register $ra

blt  Rsrc1, Src2, label
Branch on Less Than
- Conditionally branch to the instruction at
the label if the contents of register Rsrc1
are less than Src2

bltu  Rsrc1, Src2, label
Branch on Less Than Unsigned
- Conditionally branch to the instruction at
the label if the contents of register Rsrc1
are less than Src2

bltz  Rsrc, label
Branch on Less Than Zero
- Conditionally branch to the instruction at
the label if the contents of Rsrc are less
than 0

bne  Rsrc1, Src2, label
Branch on Not Equal
- Conditionally branch to the instruction at
the label if the contents of register Rsrc1
are not equal to Src2

bnez  Rsrc, label
Branch on Not Equal Zero
- Conditionally branch to the instruction at
the label if the contents of Rsrc are not
equal to 0

j  label
Jump
- Unconditionally jump to the instruction
at the label
Appendix C – MIPS Instruction Set

jal label
Jump and Link
- Unconditionally jump to the instruction at the label or whose address is in register Rs. Saves the address of the next instruction in register $ra

jalr Rscc
Jump and Link Register
- Unconditionally jump to the instruction at the label or whose address is in register Rscc. Saves the address of the next instruction in register $ra

jr Rscc
Jump Register
- Unconditionally jump to the instruction whose address is in register Rscc

14.4 Load Instructions
Below are a summary of the basic load instructions.

la Rdest, address
Load Address
- Load computed address, not the contents of the location, into register Rdest

lb Rdest, address
Load Byte
- Load the byte at address into register Rdest. The byte is sign-extended by the lb, but not the lbu, instruction

lbu Rdest, address
Load Unsigned Byte
- Load the byte at address into register Rdest. The byte is sign-extended by the lb, but not the lbu, instruction

ld Rdest, address
Load Double-Word
- Load the 64-bit quantity at address into registers Rdest and Rdest + 1
Appendix C – MIPS Instruction Set

lh  Rdest, address
Load Halfword
- Load the 16-bit quantity (halfword) at address into register Rdest. The halfword is sign-extended

lhu  Rdest, address
Load Unsigned Halfword
- Load the 16-bit quantity (halfword) at address into register Rdest. The halfword is not sign-extended

lw  Rdest, address
Load Word
- Load the 32-bit quantity (word) at address into register Rdest

lwcz  Rdest, address
Load Word Co-processor z
- Load the word at address into register Rdest of co-processor z (0-3)

lwl  Rdest, address
Load Word Left
- Load the left bytes from the word at the possibly-unaligned address into register Rdest

lwr  Rdest, address
Load Word Right
- Load the right bytes from the word at the possibly-unaligned address into register Rdest

ulh  Rdest, address
Unaligned Load Halfword
- Load the 16-bit quantity (halfword) at the possibly-unaligned address into register Rdest. The halfword is sign-extended.
ulhu Rdest, address

Unaligned Load Halfword Unsigned
- Load the 16-bit quantity (halfword) at the possibly-unaligned address into register Rdest. The halfword is not sign-extended.

ulw Rdest, address

Unaligned Load Word
- Load the 32-bit quantity (word) at the possibly-unaligned address into register Rdest.

li Rdest, imm

Load Immediate
- Move the immediate imm into register Rdest.

lui Rdest, imm

Load Upper Immediate
- Load the lower halfword of the immediate imm into the upper halfword of register Rdest. The lower bits of the register are set to 0.

14.5 Logical Instructions

Below are a summary of the basic logical instructions.

and Rdest, Rsrl, Src2

AND

andi Rdest, Rsrl, Imm

AND Immediate
- Put the logical AND of the integers from register Rsrl and Src2 (or Imm) into register Rdest.

nor Rdest, Rsrl, Src2

NOR
- Put the logical NOR of the integers from register Rsrl and Src2 into register Rdest.
Appendix C – MIPS Instruction Set

**not** \( \text{Rdest, Rsrc} \)  
**NOT**  
- Put the bitwise logical negation of the integer from register \( \text{Rsrc} \) into register \( \text{Rdest} \)

**or** \( \text{Rdest, Rsrc1, Src2} \)  
**OR**  
- Put the logical OR of the integers from register \( \text{Rsrc1} \) and \( \text{Src2} \) into register \( \text{Rdest} \)

**ori** \( \text{Rdest, Rsrc1, Imm} \)  
**OR Immediate**  
- Put the logical OR of the integers from register \( \text{Rsrc1} \) and \( \text{Imm} \) into register \( \text{Rdest} \)

**rol** \( \text{Rdest, Rsrc1, Src2} \)  
**Rotate Left**  
- Rotate the contents of register \( \text{Rsrc1} \) left by the distance indicated by \( \text{Src2} \) and put the result in register \( \text{Rdest} \)

**ror** \( \text{Rdest, Rsrc1, Src2} \)  
**Rotate Right**  
- Rotate the contents of register \( \text{Rsrc1} \) left (right) by the distance indicated by \( \text{Src2} \) and put the result in register \( \text{Rdest} \)

**sll** \( \text{Rdest, Rsrc1, Src2} \)  
**Shift Left Logical**  
- Shift the contents of register \( \text{Rsrc1} \) left by the distance indicated by \( \text{Src2} \) and put the result in register \( \text{Rdest} \)

**sllv** \( \text{Rdest, Rsrc1, Rsrc2} \)  
**Shift Left Logical Variable**  
- Shift the contents of register \( \text{Rsrc1} \) left by the distance indicated by \( \text{Rsrc2} \) and put the result in register \( \text{Rdest} \)

**sra** \( \text{Rdest, Rsrc1, Src2} \)  
**Shift Right Arithmetic**  
- Shift the contents of register \( \text{Rsrc1} \) right by the distance indicated by \( \text{Src2} \) and put the result in register \( \text{Rdest} \)
srav  Rdest, Rscl, Rscl2  Shift Right Arithmetic Variable
- Shift the contents of register Rscl right
  by the distance indicated by Rscl2 and put
  the result in register Rdest

srl  Rdest, Rscl, Src2  Shift Right Logical
- Shift the contents of register Rscl right
  by the distance indicated by Src2 and put
  the result in register Rdest

srlv  Rdest, Rscl, Rscl2  Shift Right Logical Variable
- Shift the contents of register Rscl right
  by the distance indicated by Rscl2 and put
  the result in register Rdest

xor  Rdest, Rscl, Src2  XOR
- Put the logical XOR of the integers from
  register Rscl and Src2 into register Rdest

xori  Rdest, Rscl, Imm  XOR Immediate
- Put the logical XOR of the integers from
  register Rscl and Imm into register Rdest

14.6   Store Instructions
Below are a summary of the basic store instructions.

sb  Rscl, address  Store Byte
- Store the low byte from register Rscl at
  address

sd  Rscl, address  Store Double-Word
- Store the 64-bit quantity in registers Rscl
  and Rscl + 1 at address
Appendix C – MIPS Instruction Set

\[
\begin{align*}
\text{sh} & \quad \text{Rsrc, address} & \text{Store Halfword} \\
& & - \text{Store the low halfword from register Rsrc at address} \\
\text{sw} & \quad \text{Rsrc, address} & \text{Store Word} \\
& & - \text{Store the word from register Rsrc at address} \\
\text{swcz} & \quad \text{Rsrc, address} & \text{Store Word Co-processor z} \\
& & - \text{Store the word from register Rsrc of co-processor z at address} \\
\text{swl} & \quad \text{Rsrc, address} & \text{Store Word Left} \\
& & - \text{Store the left bytes from register Rsrc at the possibly-unaligned address} \\
\text{swr} & \quad \text{Rsrc, address} & \text{Store Word Right} \\
& & - \text{Store the right bytes from register Rsrc at the possibly-unaligned address} \\
\text{ush} & \quad \text{Rsrc, address} & \text{Unaligned Store Halfword} \\
& & - \text{Store the low halfword from register Rsrc at the possibly-unaligned address} \\
\text{usw} & \quad \text{Rsrc, address} & \text{Unaligned Store Word} \\
& & - \text{Store the word from register Rsrc at the possibly-unaligned address}
\end{align*}
\]
14.7 Data Movement Instructions

Below are a summary of the basic data movement instructions. The data movement implies data movement between registers.

**move** Rdest, Rsrc

Move the contents of Rsrc to Rdest.
- The multiply and divide unit produces its result in two additional registers, hi and lo. These instructions move values to and from these registers. The multiply, divide, and remainder instructions described above are pseudo-instructions that make it appear as if this unit operates on the general registers and detect error conditions such as divide by zero or overflow.

**mfhi** Rdest

Move from $hi
- Move the contents of the hi register to register Rdest

**mflo** Rdest

Move from $lo
- Move the contents of the lo register to register Rdest

**mthi** Rdest

Move to $hi
- Move the contents register Rdest to the hi register.
- *Note*, Co-processors have their own register sets. This instructions move values between these registers and the CPU's registers.

**mtlo** Rdest

Move to $lo
- Move the contents register Rdest to the lo register.
- *Note*, Co-processors have their own register sets. This instructions move values
between these registers and the CPU's registers.

**mfc1 Rdest, FRsrc**
Move From Co-processor 1
- Move the contents of co-processor 1 float register FRsrc to CPU integer register Rdest

**mfc1.d Rdest, FRsrc1**
Move Double From Co-processor 1
- Move the contents of floating point registers FRsrc1 and FRsrc1+1 to CPU integer registers Rdest and Rdest + 1

**mtc1 Rsrc, FRdest**
Move To Co-processor 1
- Move the contents of CPU integer register Rsrc to co-processor 1 float register FRdest

**mtc1.d Rsrc, FRdest**
Move To Co-processor 1
- Move the contents of CPU integer registers Rsrc and Rsrc+1 to co-processor 1 float registers Frdest and FRdest+1.

### 14.8 Floating Point Instructions
The MIPS has a floating point co-processor (numbered 1) that operates on single precision (32-bit) and double precision (64-bit) floating point numbers. This co-processor has its own registers, which are numbered $f0$ - $f31$. Because these registers are only 32-bits wide, two of them are required to hold doubles. To simplify matters, floating point operations only use even-numbered registers - including instructions that operate on single floats. Values are moved in or out of these registers a word (32-bits) at a time by lwc1, swc1, mtc1, and mfc1 instructions described above or by the l.s, l.d, s.s, and s.d pseudo-instructions described below. The flag set by floating point comparison operations is read by the CPU with its bc1t and bc1f instructions. In all instructions below, FRdest, FRsrc1, FRsrc2, and FRsrc are floating point registers (e.g., $f2$).
Appendix C – MIPS Instruction Set

```
abs.d  FRdest, FRsrc
Floating Point Absolute Value Double
- Compute the absolute value of the floating float double in register FRsrc and put it in register FRdest

abs.s  FRdest, FRsrc
Floating Point Absolute Value Single
- Compute the absolute value of the floating float single in register FRsrc and put it in register FRdest

add.d  FRdest, FRsrc1, FRsrc2
Floating Point Addition Double
- Compute the sum of the floating float doubles in registers FRsrc1 and FRsrc2 and put it in register FRdest

add.s  FRdest, FRsrc1, FRsrc2
Floating Point Addition Single
- Compute the sum of the floating float singles in registers FRsrc1 and FRsrc2 and put it in register FRdest

c.eq.d  FRsrc1, FRsrc2
Compare Equal Double
- Compare the floating point double in register FRsrc1 against the one in FRsrc2 and set the floating point condition flag true if they are equal

c.eq.s  FRsrc1, FRsrc2
Compare Equal Single
- Compare the floating point single in register FRsrc1 against the one in FRsrc2 and set the floating point condition flag true if they are equal
```
Appendix C – MIPS Instruction Set

**c.le.d** FRsrc1, FRsrc2

- Compare Less Than Equal Double
  - Compare the floating point double in register FRsrc1 against the one in FRsrc2 and set the floating point condition flag true if the first is less than or equal to the second

**c.le.s** FRsrc1, FRsrc2

- Compare Less Than Equal Single
  - Compare the floating point single precision in register FRsrc1 against the one in FRsrc2 and set the floating point condition flag true if the first is less than or equal to the second

**c.lt.d** FRsrc1, FRsrc2

- Compare Less Than Double
  - Compare the floating point double in register FRsrc1 against the one in FRsrc2 and set the condition flag true if the first is less than the second

**c.lt.s** FRsrc1, FRsrc2

- Compare Less Than Single
  - Compare the floating point single in register FRsrc1 against the one in FRsrc2 and set the condition flag true if the first is less than the second

**cvt.d.s** FRdest, FRsrc

- Convert Single to Double
  - Convert the single precision floating point number in register FRsrc to a double precision number and put it in register FRdest

**cvt.d.w** FRdest, FRsrc

- Convert Integer to Double
  - Convert the integer in register FRsrc to a double precision number and put it in register FRdest
Appendix C – MIPS Instruction Set

**cvt.s.d** `FRdest, FRsrc`
- Convert Double to Single
  - Convert the double precision floating point number in register `FRsrc` to a single precision number and put it in register `FRdest`

**cvt.s.w** `FRdest, FRsrc`
- Convert Integer to Single
  - Convert the integer in register `FRsrc` to a single precision number and put it in register `FRdest`

**cvt.w.d** `FRdest, FRsrc`
- Convert Double to Integer
  - Convert the double precision floating point number in register `FRsrc` to an integer and put it in register `FRdest`

**cvt.w.s** `FRdest, FRsrc`
- Convert Single to Integer
  - Convert the single precision floating point number in register `FRsrc` to an integer and put it in register `FRdest`

**div.d** `FRdest, FRsrc1, FRsrc2`
-Floating Point Divide Double
  - Compute the quotient of the floating float doubles in registers `FRsrc1` and `FRsrc2` and put it in register `FRdest`.

**div.s** `FRdest, FRsrc1, FRsrc2`
- Floating Point Divide Single
  - Compute the quotient of the floating float singles in registers `FRsrc1` and `FRsrc2` and put it in register `FRdest`.

**l.d** `FRdest, address`
- Load Floating Point Double
  - Load the floating float double at address into register `FRdest`
<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>l.s  FRdest, address</code></td>
<td>Load Floating Point Single</td>
</tr>
<tr>
<td></td>
<td>- Load the floating float single at address into register FRdest</td>
</tr>
<tr>
<td><code>mov.d  FRdest, FRsrc</code></td>
<td>Move Floating Point Double</td>
</tr>
<tr>
<td></td>
<td>- Move the floating float double from register FRsrc to register FRdest</td>
</tr>
<tr>
<td><code>mov.s  FRdest, FRsrc</code></td>
<td>Move Floating Point Single</td>
</tr>
<tr>
<td></td>
<td>- Move the floating float single from register FRsrc to register FRdest</td>
</tr>
<tr>
<td><code>mul.d  FRdest, FRsrc1, FRsrc2</code></td>
<td>Floating Point Multiply Double</td>
</tr>
<tr>
<td></td>
<td>- Compute the product of the floating float doubles in registers FRsrc1 and FRsrc2 and put it in register FRdest</td>
</tr>
<tr>
<td><code>mul.s  FRdest, FRsrc1, FRsrc2</code></td>
<td>Floating Point Multiply Single</td>
</tr>
<tr>
<td></td>
<td>- Compute the product of the floating float singles in registers FRsrc1 and FRsrc2 and put it in register FRdest</td>
</tr>
<tr>
<td><code>neg.d  FRdest, FRsrc</code></td>
<td>Negate Double</td>
</tr>
<tr>
<td></td>
<td>- Store the floating float double in register FRdest at address</td>
</tr>
<tr>
<td><code>neg.s  FRdest, FRsrc</code></td>
<td>Negate Single</td>
</tr>
<tr>
<td></td>
<td>Store the floating float single in register FRdest at address</td>
</tr>
<tr>
<td><code>s.d  FRdest, address</code></td>
<td>Store Floating Point Double</td>
</tr>
<tr>
<td></td>
<td>- Store the floating float double in register FRdest at address</td>
</tr>
<tr>
<td><code>s.s  FRdest, address</code></td>
<td>Store Floating Point Single</td>
</tr>
<tr>
<td></td>
<td>- Store the floating float single in register FRdest at address</td>
</tr>
</tbody>
</table>
Floating Point Subtract Double
- Compute the difference of the floating float doubles in registers FRsrc1 and FRsrc2 and put it in register FRdest

Floating Point Subtract Single
- Compute the difference of the floating float singles in registers FRsrc1 and FRsrc2 and put it in register FRdest

### 14.9 Exception and Trap Handling Instructions

Below are a summary of the exception and trap instructions.

**rfe**
- Return From Exception
  - Restore the Status register

**syscall**
- System Call
  - Transfer control to system routine.
  - Register $v0 contains the number of the system call

**break n**
- Break
  - Cause exception n.
  - Note, Exception 1 is reserved for the debugger

**nop**
- No operation
  - Do nothing
## Appendix D – ASCII Table

This appendix provides a copy of the ASCII Table for reference.

<table>
<thead>
<tr>
<th>Char</th>
<th>Dec</th>
<th>Hex</th>
<th>Char</th>
<th>Dec</th>
<th>Hex</th>
<th>Char</th>
<th>Dec</th>
<th>Hex</th>
<th>Char</th>
<th>Dec</th>
<th>Hex</th>
</tr>
</thead>
<tbody>
<tr>
<td>NUL</td>
<td>0</td>
<td>0x00</td>
<td>spc</td>
<td>32</td>
<td>0x20</td>
<td>@</td>
<td>64</td>
<td>0x40</td>
<td>`</td>
<td>96</td>
<td>0x60</td>
</tr>
<tr>
<td>SOH</td>
<td>1</td>
<td>0x01</td>
<td>!</td>
<td>33</td>
<td>0x21</td>
<td>A</td>
<td>65</td>
<td>0x41</td>
<td>a</td>
<td>97</td>
<td>0x61</td>
</tr>
<tr>
<td>STX</td>
<td>2</td>
<td>0x02</td>
<td>&quot;</td>
<td>34</td>
<td>0x22</td>
<td>B</td>
<td>66</td>
<td>0x42</td>
<td>b</td>
<td>98</td>
<td>0x62</td>
</tr>
<tr>
<td>ETX</td>
<td>3</td>
<td>0x03</td>
<td>#</td>
<td>35</td>
<td>0x23</td>
<td>C</td>
<td>67</td>
<td>0x43</td>
<td>c</td>
<td>99</td>
<td>0x63</td>
</tr>
<tr>
<td>EOT</td>
<td>4</td>
<td>0x04</td>
<td>$</td>
<td>36</td>
<td>0x24</td>
<td>D</td>
<td>68</td>
<td>0x44</td>
<td>d</td>
<td>100</td>
<td>0x64</td>
</tr>
<tr>
<td>ENQ</td>
<td>5</td>
<td>0x05</td>
<td>%</td>
<td>37</td>
<td>0x25</td>
<td>E</td>
<td>69</td>
<td>0x45</td>
<td>e</td>
<td>101</td>
<td>0x65</td>
</tr>
<tr>
<td>ACK</td>
<td>6</td>
<td>0x06</td>
<td>&amp;</td>
<td>38</td>
<td>0x26</td>
<td>F</td>
<td>70</td>
<td>0x46</td>
<td>f</td>
<td>102</td>
<td>0x66</td>
</tr>
<tr>
<td>BEL</td>
<td>7</td>
<td>0x07</td>
<td>'</td>
<td>39</td>
<td>0x27</td>
<td>G</td>
<td>71</td>
<td>0x47</td>
<td>g</td>
<td>103</td>
<td>0x67</td>
</tr>
<tr>
<td>BS</td>
<td>8</td>
<td>0x08</td>
<td>(</td>
<td>40</td>
<td>0x28</td>
<td>H</td>
<td>72</td>
<td>0x48</td>
<td>h</td>
<td>104</td>
<td>0x68</td>
</tr>
<tr>
<td>TAB</td>
<td>9</td>
<td>0x09</td>
<td>)</td>
<td>41</td>
<td>0x29</td>
<td>I</td>
<td>73</td>
<td>0x49</td>
<td>i</td>
<td>105</td>
<td>0x69</td>
</tr>
<tr>
<td>LF</td>
<td>10</td>
<td>0x0A</td>
<td>*</td>
<td>42</td>
<td>0x2A</td>
<td>J</td>
<td>74</td>
<td>0x4A</td>
<td>j</td>
<td>106</td>
<td>0x6A</td>
</tr>
<tr>
<td>VT</td>
<td>11</td>
<td>0x0B</td>
<td>+</td>
<td>43</td>
<td>0x2B</td>
<td>K</td>
<td>75</td>
<td>0x4B</td>
<td>k</td>
<td>107</td>
<td>0x6B</td>
</tr>
<tr>
<td>FF</td>
<td>12</td>
<td>0x0C</td>
<td>,</td>
<td>44</td>
<td>0x2C</td>
<td>L</td>
<td>76</td>
<td>0x4C</td>
<td>l</td>
<td>108</td>
<td>0x6C</td>
</tr>
<tr>
<td>CR</td>
<td>13</td>
<td>0x0D</td>
<td>-</td>
<td>45</td>
<td>0x2D</td>
<td>M</td>
<td>77</td>
<td>0x4D</td>
<td>m</td>
<td>109</td>
<td>0x6D</td>
</tr>
<tr>
<td>SO</td>
<td>14</td>
<td>0x0E</td>
<td>.</td>
<td>46</td>
<td>0x2E</td>
<td>N</td>
<td>78</td>
<td>0x4E</td>
<td>n</td>
<td>110</td>
<td>0x6E</td>
</tr>
<tr>
<td>SI</td>
<td>15</td>
<td>0x0F</td>
<td>/</td>
<td>47</td>
<td>0x2F</td>
<td>O</td>
<td>79</td>
<td>0x4F</td>
<td>o</td>
<td>111</td>
<td>0x6F</td>
</tr>
<tr>
<td>DLE</td>
<td>16</td>
<td>0x10</td>
<td>0</td>
<td>48</td>
<td>0x30</td>
<td>P</td>
<td>80</td>
<td>0x50</td>
<td>p</td>
<td>112</td>
<td>0x70</td>
</tr>
<tr>
<td>DC1</td>
<td>17</td>
<td>0x11</td>
<td>1</td>
<td>49</td>
<td>0x31</td>
<td>Q</td>
<td>81</td>
<td>0x51</td>
<td>q</td>
<td>113</td>
<td>0x71</td>
</tr>
<tr>
<td>DC2</td>
<td>18</td>
<td>0x12</td>
<td>2</td>
<td>50</td>
<td>0x32</td>
<td>R</td>
<td>82</td>
<td>0x52</td>
<td>r</td>
<td>114</td>
<td>0x72</td>
</tr>
<tr>
<td>DC3</td>
<td>19</td>
<td>0x13</td>
<td>3</td>
<td>51</td>
<td>0x33</td>
<td>S</td>
<td>83</td>
<td>0x53</td>
<td>s</td>
<td>115</td>
<td>0x73</td>
</tr>
<tr>
<td>DC4</td>
<td>20</td>
<td>0x14</td>
<td>4</td>
<td>52</td>
<td>0x34</td>
<td>T</td>
<td>84</td>
<td>0x54</td>
<td>t</td>
<td>116</td>
<td>0x74</td>
</tr>
<tr>
<td>NAK</td>
<td>21</td>
<td>0x15</td>
<td>5</td>
<td>53</td>
<td>0x35</td>
<td>U</td>
<td>85</td>
<td>0x55</td>
<td>u</td>
<td>117</td>
<td>0x75</td>
</tr>
<tr>
<td>SYN</td>
<td>22</td>
<td>0x16</td>
<td>6</td>
<td>54</td>
<td>0x36</td>
<td>V</td>
<td>86</td>
<td>0x56</td>
<td>v</td>
<td>118</td>
<td>0x76</td>
</tr>
<tr>
<td>ETB</td>
<td>23</td>
<td>0x17</td>
<td>7</td>
<td>55</td>
<td>0x37</td>
<td>W</td>
<td>87</td>
<td>0x57</td>
<td>w</td>
<td>119</td>
<td>0x77</td>
</tr>
<tr>
<td>CAN</td>
<td>24</td>
<td>0x18</td>
<td>8</td>
<td>56</td>
<td>0x38</td>
<td>X</td>
<td>88</td>
<td>0x58</td>
<td>x</td>
<td>120</td>
<td>0x78</td>
</tr>
</tbody>
</table>
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<p>| | | | | | | | | | | | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>EM</td>
<td>25</td>
<td>0x19</td>
<td>9</td>
<td>57</td>
<td>0x39</td>
<td>Y</td>
<td>89</td>
<td>0x59</td>
<td>y</td>
<td>121</td>
<td>0x79</td>
</tr>
<tr>
<td>SUB</td>
<td>26</td>
<td>0x1A</td>
<td>:</td>
<td>58</td>
<td>0x3A</td>
<td>Z</td>
<td>90</td>
<td>0x5A</td>
<td>z</td>
<td>122</td>
<td>0x7A</td>
</tr>
<tr>
<td>ESC</td>
<td>27</td>
<td>0x1B</td>
<td>;</td>
<td>59</td>
<td>0x3B</td>
<td>[</td>
<td>91</td>
<td>0x5B</td>
<td>{</td>
<td>123</td>
<td>0x7B</td>
</tr>
<tr>
<td>FS</td>
<td>28</td>
<td>0x1C</td>
<td>&lt;</td>
<td>60</td>
<td>0x3C</td>
<td>\</td>
<td>92</td>
<td>0x5C</td>
<td></td>
<td></td>
<td>124</td>
</tr>
<tr>
<td>GS</td>
<td>29</td>
<td>0x1D</td>
<td>=</td>
<td>61</td>
<td>0x3D</td>
<td>]</td>
<td>93</td>
<td>0x5D</td>
<td>}</td>
<td>125</td>
<td>0x7D</td>
</tr>
<tr>
<td>RS</td>
<td>30</td>
<td>0x1E</td>
<td>&gt;</td>
<td>62</td>
<td>0x3E</td>
<td>^</td>
<td>94</td>
<td>0x5E</td>
<td>~</td>
<td>126</td>
<td>0x7E</td>
</tr>
<tr>
<td>US</td>
<td>31</td>
<td>0x1F</td>
<td>?</td>
<td>63</td>
<td>0x3F</td>
<td>_</td>
<td>95</td>
<td>0x5F</td>
<td>DEL</td>
<td>127</td>
<td>0x7F</td>
</tr>
</tbody>
</table>

For additional information and a more complete listing of the ASCII codes (including the extended ASCII characters), refer to [http://www.asciitable.com/](http://www.asciitable.com/)
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<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>global declaration directive</td>
<td>68</td>
</tr>
<tr>
<td>halfword</td>
<td>4</td>
</tr>
<tr>
<td>heap</td>
<td>6</td>
</tr>
<tr>
<td>IEEE 32-bit Representation</td>
<td>14</td>
</tr>
<tr>
<td>IEEE 64-bit Representation</td>
<td>17</td>
</tr>
<tr>
<td>IEEE 754 32-bit floating-point standard</td>
<td>14</td>
</tr>
<tr>
<td>IF statement</td>
<td>40</td>
</tr>
<tr>
<td>Immediate addressing mode</td>
<td>53</td>
</tr>
<tr>
<td>Immediate value</td>
<td>26</td>
</tr>
<tr>
<td>indirect memory access</td>
<td>54</td>
</tr>
<tr>
<td>Indirection</td>
<td>54</td>
</tr>
<tr>
<td>Integer / Floating-Point Conversion</td>
<td>45</td>
</tr>
<tr>
<td>Instructions</td>
<td></td>
</tr>
<tr>
<td>Integer / Floating-Point Register Data</td>
<td></td>
</tr>
<tr>
<td>Movement</td>
<td>44</td>
</tr>
<tr>
<td>Integer Data Declarations</td>
<td>20</td>
</tr>
<tr>
<td>integer numbers</td>
<td>11</td>
</tr>
<tr>
<td>integer registers</td>
<td>6</td>
</tr>
<tr>
<td>j &lt;label&gt;</td>
<td>39</td>
</tr>
<tr>
<td>jal &lt;procName&gt;</td>
<td>69</td>
</tr>
<tr>
<td>jr $ra</td>
<td>69</td>
</tr>
<tr>
<td>l&lt;type&gt;</td>
<td>27</td>
</tr>
<tr>
<td>la</td>
<td>27</td>
</tr>
<tr>
<td>Labels</td>
<td>23</td>
</tr>
<tr>
<td>lb</td>
<td>28</td>
</tr>
<tr>
<td>Leaf procedures</td>
<td>67</td>
</tr>
<tr>
<td>Least Significant Byte</td>
<td>4</td>
</tr>
<tr>
<td>lh</td>
<td>27</td>
</tr>
<tr>
<td>li</td>
<td>27</td>
</tr>
<tr>
<td>Linkage</td>
<td>69</td>
</tr>
<tr>
<td>little-endian</td>
<td>4p</td>
</tr>
<tr>
<td>Load and Store</td>
<td>26</td>
</tr>
<tr>
<td>logical AND operation</td>
<td>34</td>
</tr>
<tr>
<td>logical NOR operation</td>
<td>34</td>
</tr>
<tr>
<td>logical NOT operation</td>
<td>34</td>
</tr>
<tr>
<td>logical OR operation</td>
<td>34</td>
</tr>
<tr>
<td>logical XOR operation</td>
<td>34</td>
</tr>
<tr>
<td>lw</td>
<td>27</td>
</tr>
<tr>
<td>main procedure</td>
<td>23</td>
</tr>
<tr>
<td>Memory</td>
<td>4</td>
</tr>
<tr>
<td>memory layout</td>
<td>6</td>
</tr>
<tr>
<td>mfc1</td>
<td>44</td>
</tr>
<tr>
<td>mfc1.d</td>
<td>44</td>
</tr>
<tr>
<td>mfhi</td>
<td>28</td>
</tr>
<tr>
<td>mflo</td>
<td>28</td>
</tr>
<tr>
<td>MIPS Calling Conventions</td>
<td>67</td>
</tr>
<tr>
<td>miscellaneous registers</td>
<td>8</td>
</tr>
<tr>
<td>Most Significant Byte</td>
<td>4</td>
</tr>
<tr>
<td>move</td>
<td>28</td>
</tr>
<tr>
<td>Move</td>
<td>28</td>
</tr>
<tr>
<td>mtc1</td>
<td>44</td>
</tr>
<tr>
<td>mtc1.d</td>
<td>44</td>
</tr>
<tr>
<td>mthi</td>
<td>28</td>
</tr>
<tr>
<td>mtlo</td>
<td>28</td>
</tr>
<tr>
<td>mul</td>
<td>29</td>
</tr>
<tr>
<td>mulo</td>
<td>29</td>
</tr>
<tr>
<td>mulo</td>
<td>29</td>
</tr>
<tr>
<td>mulou</td>
<td>29</td>
</tr>
<tr>
<td>mult</td>
<td>29</td>
</tr>
<tr>
<td>Multi-dimension Array Implementation</td>
<td>93</td>
</tr>
<tr>
<td>Multiple push's/pop's</td>
<td>63</td>
</tr>
<tr>
<td>mulu</td>
<td>30</td>
</tr>
<tr>
<td>neg</td>
<td>30</td>
</tr>
<tr>
<td>negu</td>
<td>30</td>
</tr>
<tr>
<td>Non-leaf procedures</td>
<td>67</td>
</tr>
<tr>
<td>nor</td>
<td>34</td>
</tr>
<tr>
<td>normalized scientific notation</td>
<td>15</td>
</tr>
<tr>
<td>not</td>
<td>34</td>
</tr>
<tr>
<td>Notational Conventions</td>
<td>25</td>
</tr>
<tr>
<td>Operand Notation</td>
<td>25</td>
</tr>
<tr>
<td>operands</td>
<td>25</td>
</tr>
<tr>
<td>operation</td>
<td>25</td>
</tr>
<tr>
<td>or</td>
<td>34</td>
</tr>
<tr>
<td>Pop</td>
<td>63</td>
</tr>
<tr>
<td>pop operation</td>
<td>61</td>
</tr>
<tr>
<td>Print Character</td>
<td>86</td>
</tr>
<tr>
<td>Print Double</td>
<td>85</td>
</tr>
</tbody>
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